



Test Report: ELG-240-24

240W Constant Voltage + Constant Current LED Driver

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

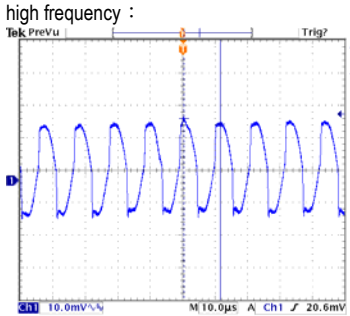
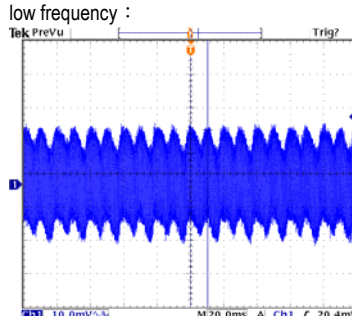
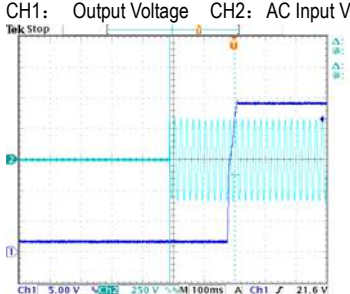
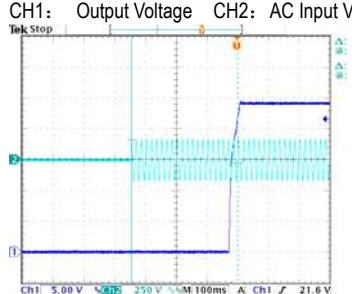
E.M.C. Test

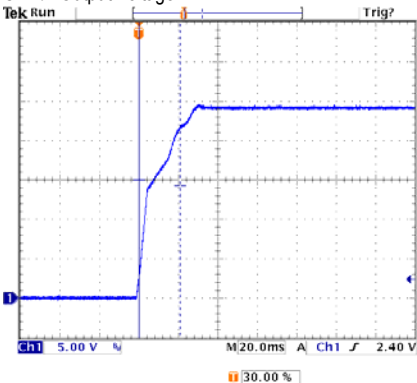
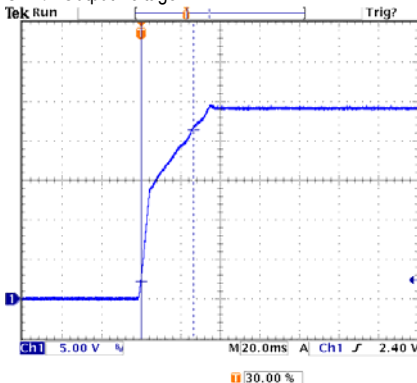
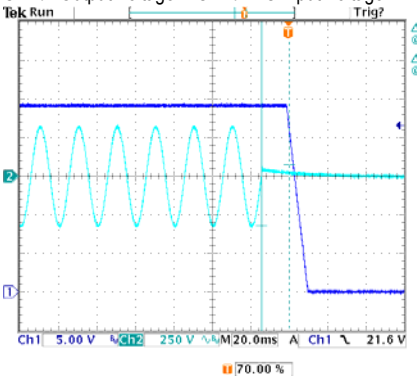
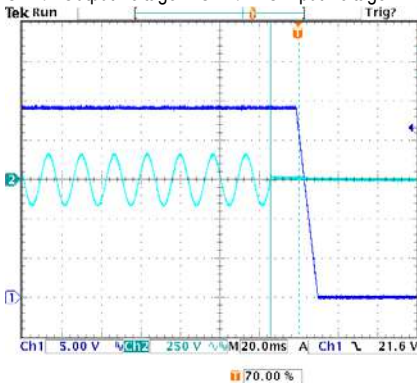
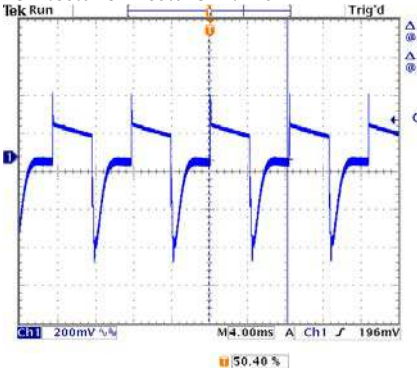
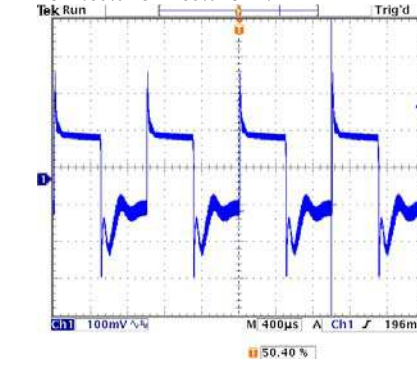
■ RELIABILITY TEST

Environment Test

DESIGN VERIFY TEST

OUTPUT FUNCTION TEST

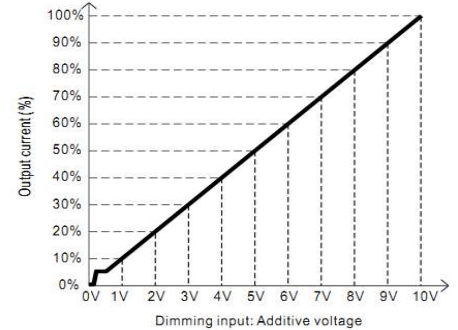
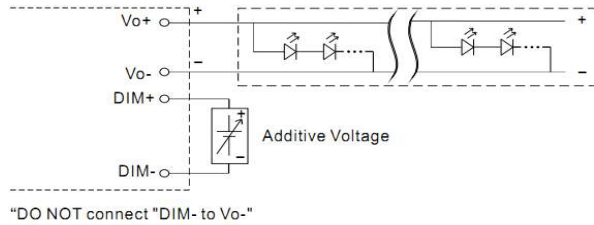
NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	CONSTANT CURRENT REGION	12V~24V	I/P: 230VAC O/P: LED MODE Ta: 25°C	7 V~ 24 V
2	OUTPUT VOLTAGE ADJUST RANGE (For A-Type only)	22.4V~25.6V	I/P: 230VAC O/P: NO LOAD Ta: 25°C	21.32 V~ 26.73 V
3	OUTPUT CURRENT ADJUST RANGE (For A-Type only)	5A~10A	I/P: 230VAC O/P: SETTING Ta: 25°C	4.17 A~ 12.33 A
4	OUTPUT VOLTAGE TOLERANCE	-2%~+2%	I/P: 100VAC / 305VAC O/P: 95% / NO LOAD Ta: 25°C	-0.29%~ 0.58%
5	LINE REGULATION	-0.5%~+0.5%	I/P: 200VAC ~ 305VAC O/P: 95% LOAD Ta: 25°C	0%~ 0%
6	LOAD REGULATION	-0.5%~+0.5%	I/P: 230VAC O/P: 95% ~NO LOAD Ta: 25°C	-0.29%~ 0.37%
7	OVER/UNDERSHOOT TEST	<± 5 %	I/P: 230VAC O/P: 95% LOAD Ta: 25°C	<5 %
8	RIPPLE & NOISE (Max)	200mVp-p	I/P: 230VAC O/P: 95% LOAD Ta: 25°C	37 mVp-p
high frequency :  Δ: 1.80mV @: 17.4mV Δ: 89.3kHz @: 89.3kHz Ch1 Pk-Pk 31.4mV low frequency :  Δ: 7.00mV @: 16.4mV Δ: 100 Hz @: 104 Hz Ch1 Pk-Pk 37.0mV				
9	SET UP TIME(Max)	230VAC/ 500ms 115VAC/ 1000ms	I/P: 230 VAC I/P: 115 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	230VAC/ 212 ms 115VAC/ 346 ms
INPUT=230VAC/50HZ @ 95% LOAD CH1: Output Voltage CH2: AC Input Voltage  Δ: 125 V @: 0.00 V Δ: 212ms @: -212ms INPUT=115VAC/60HZ @ 75% LOAD CH1: Output Voltage CH2: AC Input Voltage  Δ: 195 V @: 160 V Δ: 346ms @: -346ms				

10	RISE TIME (Max)	230VAC/ 100ms 115VAC/ 100ms	I/P: 230 VAC I/P: 115 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	230VAC/ 20.8 ms 115VAC/ 26.4 ms
INPUT=230VAC/50HZ @ 95% LOAD CH1: Output Voltage  Δ: 700mV @: 15.0 V Δ: 20.8ms @: 0.00 s INPUT=115VAC/60HZ @ 75% LOAD CH1: Output Voltage  Δ: 19.2 V @: 2.20 V Δ: 26.4ms @: 0.00 s				
11	HOLD UP TIME(Typ)	230VAC/ 10ms 115VAC/ 10ms	I/P: 230 VAC I/P: 115 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	230VAC/ 14.4 ms 115VAC/ 14.4 ms
INPUT=230VAC/50HZ @ 95% LOAD CH1: Output Voltage CH2: AC Input Voltage  Δ: 39.5 V @: -3.20 V Δ: 14.4ms @: -14.4ms INPUT=115VAC/60HZ @ 75% LOAD CH1: Output Voltage CH2: AC Input Voltage  Δ: 25.0 V @: -5.00 V Δ: 14.4ms @: -14.4ms				
12	DYNAMIC LOAD	V1: 2400 mVp-p	I/P: 230VAC O/P: (1)95%/50% LOAD 50%DUTY / 120HZ (2)95%/50% LOAD 50%DUTY / 1KHZ Ta: 25°C	(1) 880mVp-p (2) 548mVp-p
FULL /50% LOAD 50%DUTY / 120HZ  Δ: 4.00mV @: -12.0mV Δ: 120 Hz @: 124 Hz Ch1 Pk-Pk 880mV FULL /50% LOAD 50%DUTY / 1KHZ  Δ: 18.0mV @: -68.0mV Δ: 1.00kHz @: 1.02kHz Ch1 Pk-Pk 548mV				

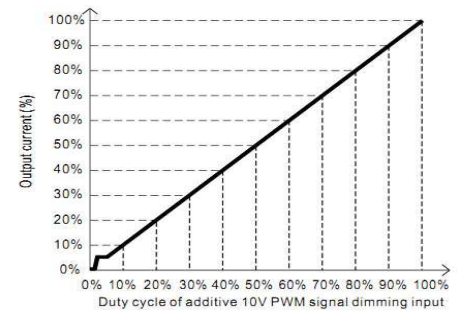
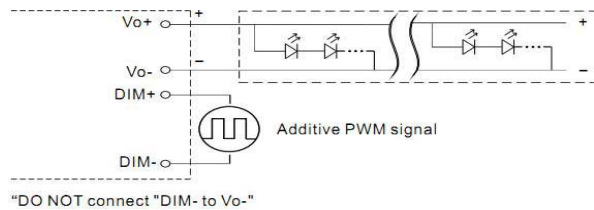
13 DIMMING OPERATION (for B-Type)

- Output constant current level can be adjusted by applying one of the three methodologies between DIM+ and DIM-: 0 ~ 10Vdc, or 10V PWM signal or resistance.
- Direct connecting to LEDs is suggested. It is not suitable to be used with additional drivers.
- Dimming source current from power supply: 100uA(typ.)

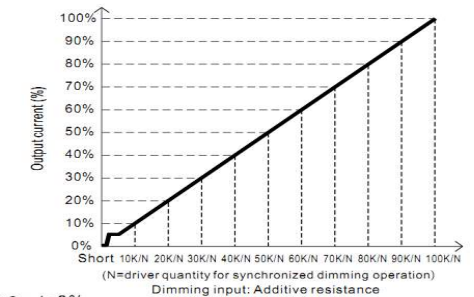
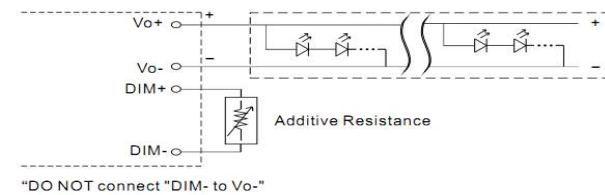
◎ Applying additive 0 ~ 10VDC



◎ Applying additive 10V PWM signal (frequency range 100Hz ~ 3KHz):



◎ Applying additive resistance:



Note : 1. Min. dimming level is about 8% and the output current is not defined when $0\% < I_{out} < 8\%$.
2. The output current could drop down to 0% when dimming input is about 0k Ω or 0Vdc, or 10V PWM signal with 0% duty cycle.

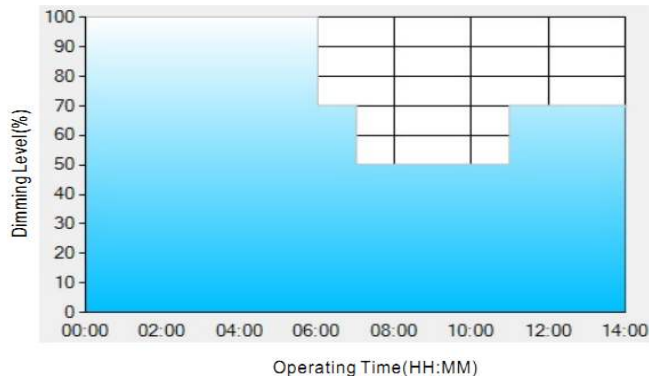
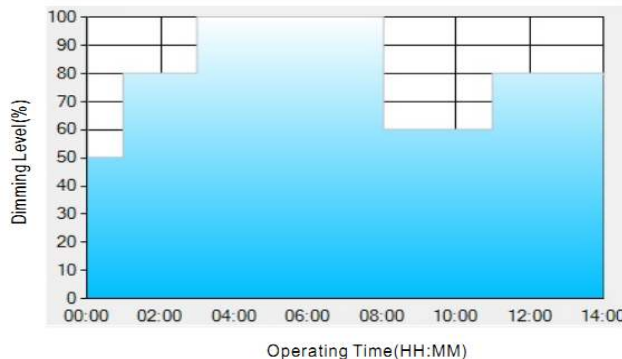
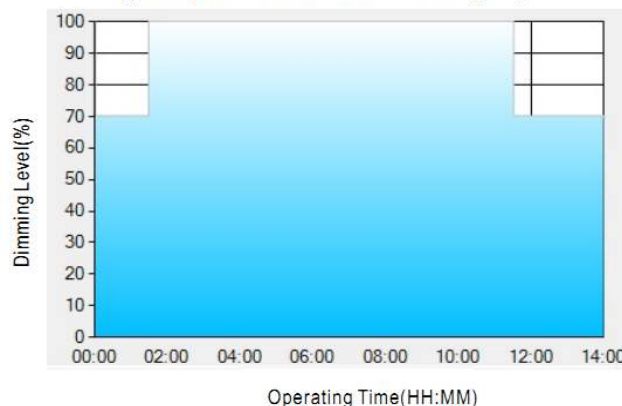
I/P: 230 VAC

O/P: DIMMING TEST

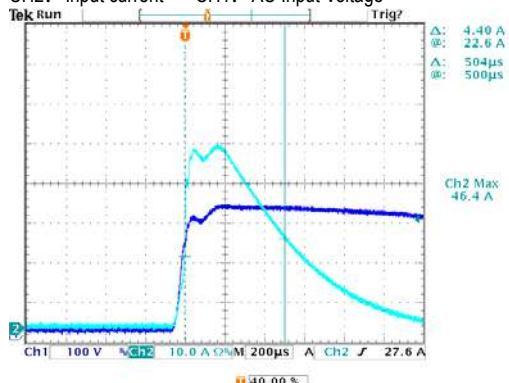
Ta: 25°C

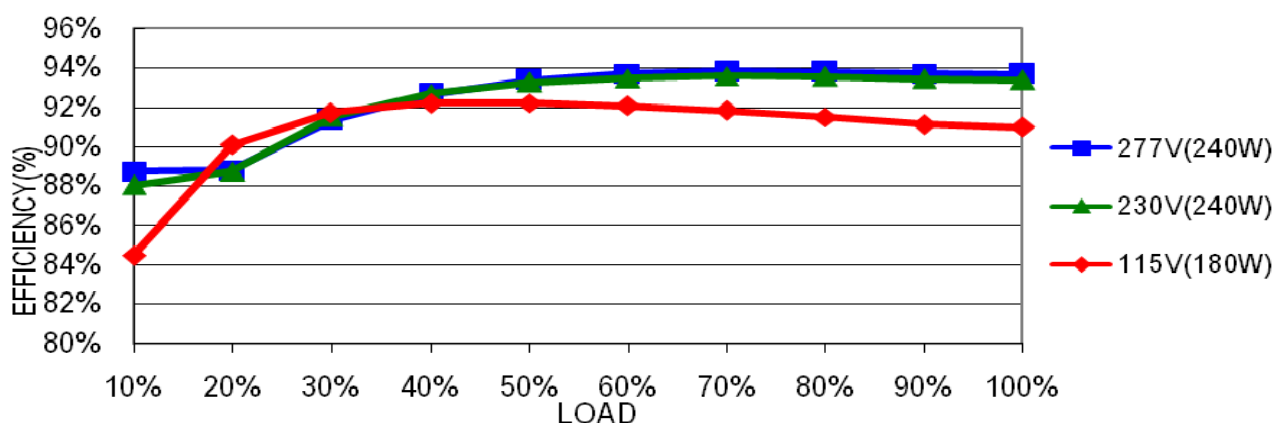
	V	Short	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN
1	Output Current	0	0.984	1.979	2.979	3.96	4.947	5.936	6.921	7.908	8.895	9.88	10.125
	%	0%	9.84%	19.79%	29.79%	39.60%	49.47%	59.36%	69.21%	79.08%	88.95%	98.80%	101.25%
2	PWM(100Hz)	0V	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN
	Output Current	0	1.21	2.204	3.152	4.125	5.065	6.046	6.987	7.967	8.908	9.74	10.125
	%	0%	12.10%	22.04%	31.52%	41.25%	50.65%	60.46%	69.87%	79.67%	89.08%	97.40%	101.25%
3	R	0%	10K	20K	30K	40K	50K	60K	70K	80K	90K	100K	OPEN
	Output Current	0	1.019	2.042	3.079	4.092	5.121	6.148	7.185	8.231	9.266	10.086	10.125
	%	0%	10.19%	20.42%	30.79%	40.92%	51.21%	61.48%	71.85%	82.31%	92.66%	100.86%	101.25%

TEST RESULT: OK

14	DIMMING OPERATION (primary side;for DA-Type)	※DALI Interface ·Apply DALI signal between DA+ and DA-. ·DALI protocol comprises 16 groups and 64 addresses. ·First step is fixed at 8% of output.Please contact MEAN WELL for other setup. I/P: 230 VAC O/P: DIMMING TEST Ta: 25℃ TEST RESULT: OK																																													
15	DIMMING OPERATION (for DXX-Type by User definition)	※Smart timer dimming function ·MEAN WELL Smart timer dimming primarily provides the adaptive proportion dimming profile for the output constant current level to perform up to 14 consecutive hours.3 dimming profiles hereunder are defined accounting for the most frequently seen applications.If other options may be needed,Please contact MEAN WELL for other setup. Ex : Ⓒ D01-Type: the profile recommended for residential lighting  Set up for D01-Type in Smart timer dimming software program: <table><tr><th></th><th>T1</th><th>T2</th><th>T3</th><th>T4</th></tr><tr><td>TIME**</td><td>06:00</td><td>07:00</td><td>11:00</td><td>---</td></tr><tr><td>LEVEL**</td><td>100%</td><td>70%</td><td>50%</td><td>70%</td></tr></table> Ex : Ⓒ D02-Type: the profile recommended for street lighting  Set up for D02-Type in Smart timer dimming software program: <table><tr><th></th><th>T1</th><th>T2</th><th>T3</th><th>T4</th><th>T5</th></tr><tr><td>TIME**</td><td>01:00</td><td>03:00</td><td>8:00</td><td>11:00</td><td>---</td></tr><tr><td>LEVEL**</td><td>50%</td><td>80%</td><td>100%</td><td>60%</td><td>80%</td></tr></table> Ex : Ⓒ D03-Type: the profile recommended for tunnel lighting  Set up for D03-Type in Smart timer dimming software program: <table><tr><th></th><th>T1</th><th>T2</th><th>T3</th></tr><tr><td>TIME**</td><td>01:30</td><td>11:00</td><td>---</td></tr><tr><td>LEVEL**</td><td>70%</td><td>100%</td><td>70%</td></tr></table> I/P: 230 VAC O/P: DIMMING TEST Ta: 25℃ TEST RESULT: OK		T1	T2	T3	T4	TIME**	06:00	07:00	11:00	---	LEVEL**	100%	70%	50%	70%		T1	T2	T3	T4	T5	TIME**	01:00	03:00	8:00	11:00	---	LEVEL**	50%	80%	100%	60%	80%		T1	T2	T3	TIME**	01:30	11:00	---	LEVEL**	70%	100%	70%
	T1	T2	T3	T4																																											
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	T1	T2	T3																																												
TIME**	01:30	11:00	---																																												
LEVEL**	70%	100%	70%																																												

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	100VAC~305VAC	I/P: TESTING O/P: 95% LOAD Ta: 25°C	97 V~ 305 V
			I/P: LOW-LINE-3V=97 V HIGH-LINE+10V=315 V O/P: 95%/NO LOAD ON: 30 Sec OFF: 30 Sec 10MIN (POWER ON/OFF NO DAMAGE)	TEST: OK
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P: 100 VAC ~305 VAC O/P: 95%-NO LOAD Ta: 25°C	TEST: OK
3	AC CURRENT	2.2A/115VAC 1.5A/230VAC 1.2A/277VAC	I/P: 115 VAC I/P: 230 VAC I/P: 277 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	I = 2.17 A/ 115VAC I = 1.07 A/ 230VAC I = 0.90 A/ 277VAC
4	LEAKAGE CURRENT	< 0.75mA / 277VAC	I/P: 277 VAC O/P: NO LOAD Ta: 25°C	L-FG: 0.448 mA N-FG: 0.345 mA
5	NO LOAD/STANDBY POWER CONSUMPTION	< 0.5W	I/P: 230VAC O/P: NO LOAD Ta: 25°C	0.381 W/ 230VAC
6	INRUSH CURRENT(Typ)	230V/ 60A Twidth =510us measured at 50% Ipeak COLD START	I/P: 230 VAC O/P: 95% LOAD Ta: 25°C	I = 46.4 A/ 230VAC Twidth =504 us
INPUT=230VAC/50HZ @ 95% LOAD CH2: Input current CH1: AC Input Voltage 				
7	EFFICIENCY(Typ)	92%	I/P: 230VAC O/P: 95% LOAD Ta: 25°C	93.39 %

EFFICIENCY vs LOAD


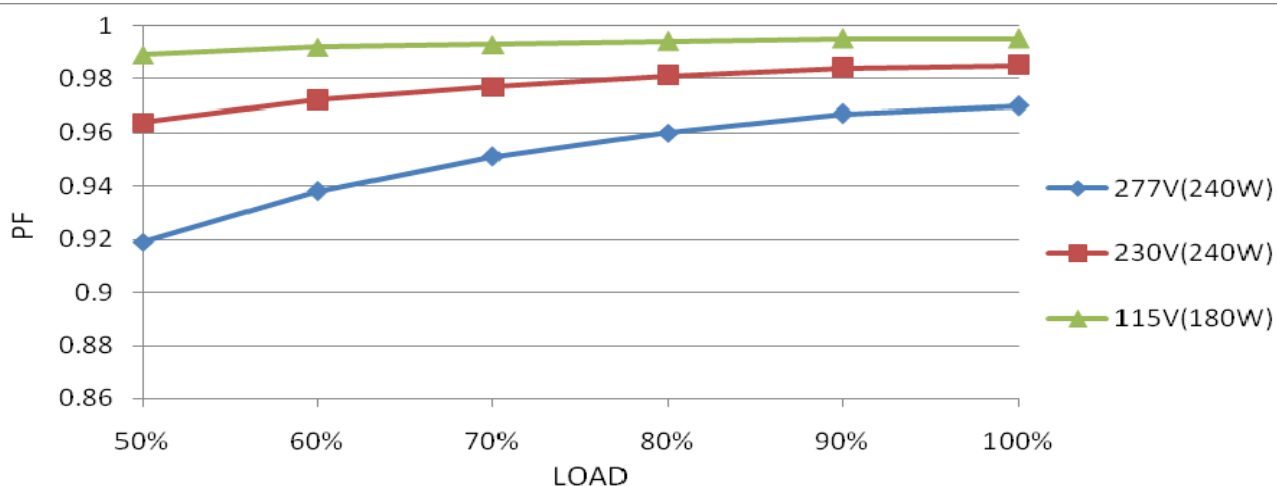
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POWER FACTOR

0.97/ 115VAC
0.95/ 230VAC
0.92/ 277VAC

I/P: 115 VAC
I/P: 230 VAC
I/P: 277 VAC
O/P: 95% LOAD/75% LOAD
Ta: 25°C

PF= 0.995 / 115VAC
PF= 0.985 / 230VAC
PF= 0.970 / 277VAC

P.F vs LOAD


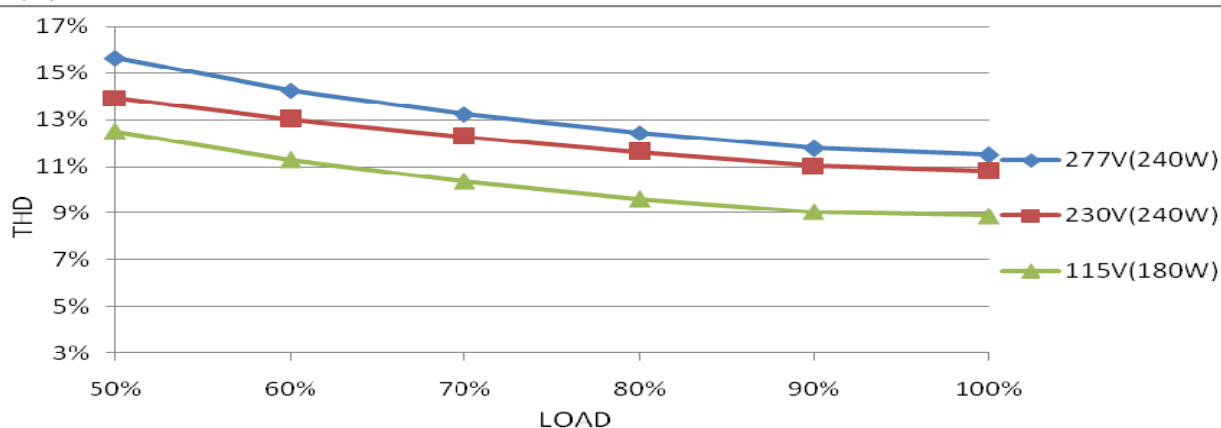
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TOTAL HARMONIC DISTORTION

THD<20%
(@load≥50%/115VAC, 230VAC;
@load≥75%/277VAC)

I/P: 115 VAC/50% LOAD
I/P: 230 VAC/50% LOAD
I/P: 277 VAC/75% LOAD
Ta: 25°C

THD=12.51% @50% load /115VAC
THD=13.92% @50% load /230VAC
THD=12.86% @75% load /277VAC

THD vs LOAD


PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER CURRENT PROTECTION	95%~108%	I/P: 200VAC I/P: 230VAC I/P: 305VAC O/P: TESTING Ta: 25°C	101.25 %/ 200VAC 101.25 %/ 230VAC 101.25 %/ 305VAC Constant Current Limiting, recovers automatically after fault condition is removed
2	OVER VOLTAGE PROTECTION	27V~34V	I/P: 100VAC I/P: 230VAC I/P: 305VAC O/P: NO LOAD Ta: 25°C	30.76 V/ 100VAC 30.74 V/ 230VAC 30.76 V/ 305VAC Shut down o/p voltage, re-power on to recovery
3	OVER TEMPERATURE PROTECTION	NO DAMAGE	I/P: 200VAC I/P: 230VAC I/P: 305VAC O/P: 95% LOAD	O.T.P. Active Shut down o/p voltage, re-power on to recovery
4	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 200VAC I/P: 305VAC O/P: 95% LOAD Ta: 25°C	NO DAMAGE Hiccup mode, recovers automatically after fault condition is removed

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Power Transistor	Q 2 Rated 600V/11A	I/P: High-Line +3V =308V O/P: (1) 95% LOAD Turn on (2) Output Short (3) 95% LOAD continue Ta: 25°C	(1) 496 V (2) 526 V (3) 446 V
2	O/P Diode (MOSFET)	Q101 Rated 75V/80A	I/P: High-Line +3V =308V O/P: (1) 95% LOAD Turn on (2) Output Short (3) 95% LOAD continue Ta: 25°C	(1) 57.2 V (2) 11.7 V (3) 56.6 V
3	Input Capacitor	C5 Rated 100u/ 450V	I/P: High-Line +3V =308 V O/P: (1) 95% LOAD input on/off (2) NO LOAD input on /Off (3) 95% LOAD /NO LOAD Change Ta: 25°C	(1) 448 V (2) 442 V (3) 448 V
4	Control IC	U3 Rated 20V (MAX.)	I/P: High-Line +3V =308 V O/P: ((1) 95% LOAD (2) Output Short (3) O.L.P (4) O.V.P (5) Low Line No Load Vo(min) Ta: 25°C	(1) 15.8 V (2) 15.9 V (3) 15.8 V (4) 15.2 V (5) 15.1 V
5	PFC Power Transistor	Q 3 Rated 600V/20A	I/P: High-Line +3V =308V O/P: (1) 95% LOAD Turn on (2) Output Short (3) 95% LOAD continue Ta: 25°C	(1) 596 V (2) 536 V (3) 558 V

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P: 3.75KVAC/min I/P-FG: 2.0KVAC/min O/P-FG: 1.5KVAC/min	I/P-O/P: 4.2KVAC/min I/P-FG: 2.4 KVAC/min O/P-FG: 1.8 KVAC/min Ta: 25°C	I/P-O/P: 2.471 mA I/P-FG: 2.820 mA O/P-FG: 2.416 mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P: 500VDC>100MΩ I/P-FG: 500VDC>100MΩ O/P-FG: 500VDC>100MΩ	I/P-O/P: 500 VDC I/P-FG: 500 VDC O/P-FG: 500 VDC Ta: 25°C	I/P-O/P: >9999 MΩ I/P-FG: >9999 MΩ O/P-FG: >9999 MΩ

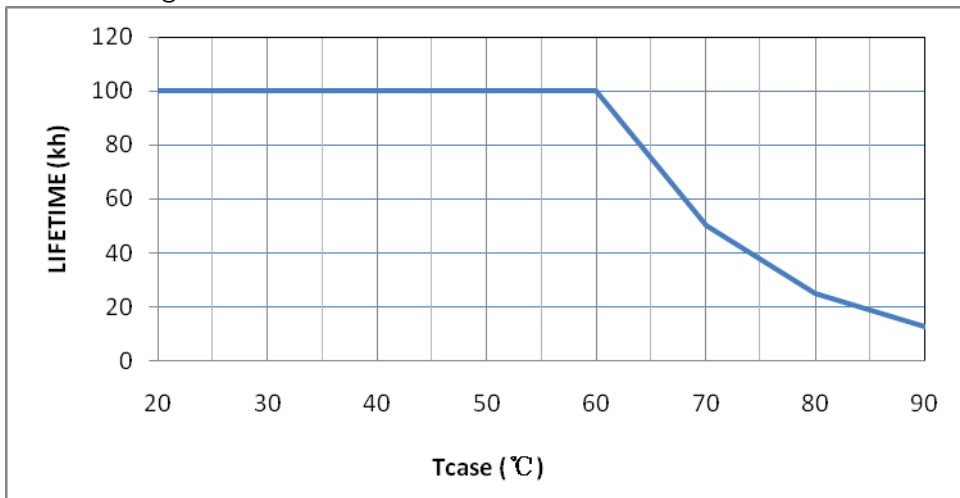
E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	HARMONIC	EN61000-3-2 CLASS C	I/P: 230VAC/50HZ O/P: 95%/50% LOAD Ta: 25°C	PASS
2	CONDUCTION	EN55015	I/P: 230 VAC (50HZ) O/P: 95% LOAD Ta: 25°C	PASS Test by certified Lab
3	RADIATION	EN55015	I/P: 230 VAC (50HZ) O/P: 95% LOAD Ta: 25°C	PASS Test by certified Lab
4	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR: 8KV Contact: 4KV	I/P: 230 VAC/50HZ O/P: 95% LOAD Ta: 25°C	CRITERIA A
5	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT: 1KV	I/P: 230VAC/50HZ O/P: 95% LOAD Ta: 25°C	CRITERIA A
6	SURGE	EN61000-4-5 INDUSTRY L-N: 4KV L,N-PE: 6KV	I/P: 230VAC/50HZ O/P: 95% LOAD L-N: 4KV L,N-PE: 6KV Ta: 25°C	CRITERIA A
7	Test by certified Lab & Test Report Prepare			

■ RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																																
1	TEMPERATURE RISE TEST	MODEL: ELG-240-24 1. ROOM AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: 95% LOAD Ta=34.6 °C 2. HIGH AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: 95% LOAD Ta=58.6 °C <table><tr><th>NO</th><th>Position</th><th>ROOM AMBIENT Ta=34.6 °C</th><th>HIGH AMBIENT Ta=58.6 °C</th></tr><tr><td>1</td><td>BD1</td><td>69.0°C</td><td>85.9°C</td></tr><tr><td>2</td><td>C7</td><td>67.6°C</td><td>84.7°C</td></tr><tr><td>3</td><td>RTH1</td><td>76.0°C</td><td>89.2°C</td></tr><tr><td>4</td><td>Q3</td><td>69.2°C</td><td>86.3°C</td></tr><tr><td>5</td><td>C5</td><td>64.7°C</td><td>80.7°C</td></tr><tr><td>6</td><td>Q1</td><td>69.5°C</td><td>87.1°C</td></tr><tr><td>7</td><td>Q2</td><td>69.6°C</td><td>87.4°C</td></tr><tr><td>8</td><td>C16</td><td>67.8°C</td><td>83.8°C</td></tr><tr><td>9</td><td>C45</td><td>64.9°C</td><td>81.6°C</td></tr><tr><td>10</td><td>U3</td><td>64.5°C</td><td>81.8°C</td></tr><tr><td>11</td><td>T1</td><td>71.6°C</td><td>87.6°C</td></tr><tr><td>12</td><td>Q101</td><td>67.6°C</td><td>84.5°C</td></tr><tr><td>13</td><td>Q102</td><td>66.3°C</td><td>83.6°C</td></tr><tr><td>14</td><td>C102</td><td>65.8°C</td><td>82.6°C</td></tr><tr><td>15</td><td>C103</td><td>63.9°C</td><td>81.0°C</td></tr><tr><td>16</td><td>C205</td><td>65.7°C</td><td>82.6°C</td></tr><tr><td>17</td><td>U101</td><td>70.9°C</td><td>87.4°C</td></tr><tr><td>18</td><td>RTH3</td><td>64.3°C</td><td>80.4°C</td></tr><tr><td>19</td><td>TC</td><td>59.7°C</td><td>77.3°C</td></tr></table>			NO	Position	ROOM AMBIENT Ta=34.6 °C	HIGH AMBIENT Ta=58.6 °C	1	BD1	69.0°C	85.9°C	2	C7	67.6°C	84.7°C	3	RTH1	76.0°C	89.2°C	4	Q3	69.2°C	86.3°C	5	C5	64.7°C	80.7°C	6	Q1	69.5°C	87.1°C	7	Q2	69.6°C	87.4°C	8	C16	67.8°C	83.8°C	9	C45	64.9°C	81.6°C	10	U3	64.5°C	81.8°C	11	T1	71.6°C	87.6°C	12	Q101	67.6°C	84.5°C	13	Q102	66.3°C	83.6°C	14	C102	65.8°C	82.6°C	15	C103	63.9°C	81.0°C	16	C205	65.7°C	82.6°C	17	U101	70.9°C	87.4°C	18	RTH3	64.3°C	80.4°C	19	TC	59.7°C	77.3°C
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2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P: 305VAC/110VAC O/P: 95% LOAD/75% LOAD Ta= -45°C / -30°C	TEST: OK																																																																																
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60°C NO DAMAGE	I/P: 305VAC O/P: 95% LOAD Ta=60°C HUMIDITY= 95 %R.H	TEST: OK																																																																																
4	TEMPERATURE COEFFICIENT	±0.03 %/°C (0~50°C)	I/P: 230 VAC O/P: 95% LOAD	±0.022 %/°C (0~50°C)																																																																																
5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature: -45°C ~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 5 CYCLE 5. Input/Output condition: STATIC		TEST: OK																																																																																

6	THERMAL SHOCK TEST	1. Thermal shock Temperature: -45℃ ~ +65℃ 2. Temperature change rate : 25℃ / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 10 CYCLE 5. Input/Output condition: 230VAC/95% LOAD AC ON/OFF TEST AC on 3 sec/AC off 1 sec TEST	TEST: OK																		
7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform: Sine Wave (2) Frequency: 10~500Hz (3) Sweep Time: 12min/sweep cycle (4) Acceleration: 5G (5) Test Time: 72min in each axis (X.Y.Z) (6) Ta: 25℃	TEST: OK																		
8	CAPACITOR LIFE CYCLE	ELG-240-24: SUPPOSE C102 IS THE MOST CRITICAL COMPONENT (1) I/P: 230VAC O/P: 95% LOAD Ta= 25 ℃ LIFE TIME (2) I/P: 230VAC O/P: 95% LOAD Ta= 60 ℃ LIFE TIME (3) I/P: 230VAC O/P: 75% LOAD Ta= 60 ℃ LIFE TIME (4) I/P: 230VAC O/P: 50% LOAD Ta= 60 ℃ LIFE TIME	(1) 434976 HRS (2) 63363 HRS (3) 68570 HRS (4) 105441 HRS																		
9	MTBF	Conducted by Parts Stress Analysis Prediction 826.7K hrs min. Telcordia SR-332 (Bellcore) 200.8K hrs min. MIL-HDBK-217F (25℃)																			
10	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure (Expected Life): Above 50000 hours @ Tc 70℃ <table><caption>Data points for Lifetime vs Tcase</caption><thead><tr><th>Tcase (°C)</th><th>Lifetime (kh)</th></tr></thead><tbody><tr><td>20</td><td>100</td></tr><tr><td>30</td><td>100</td></tr><tr><td>40</td><td>100</td></tr><tr><td>50</td><td>100</td></tr><tr><td>60</td><td>100</td></tr><tr><td>70</td><td>50</td></tr><tr><td>80</td><td>25</td></tr><tr><td>90</td><td>15</td></tr></tbody></table>		Tcase (°C)	Lifetime (kh)	20	100	30	100	40	100	50	100	60	100	70	50	80	25	90	15
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TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	ZHANGZJ/ZHUOKB	SKY	LIUWY