



Test Report: PWM-120-24

120W PWM Output LED Driver

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

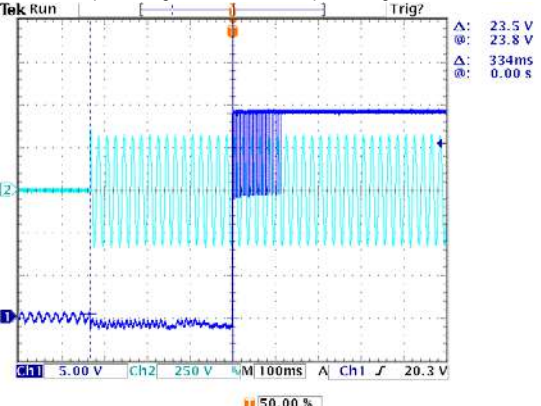
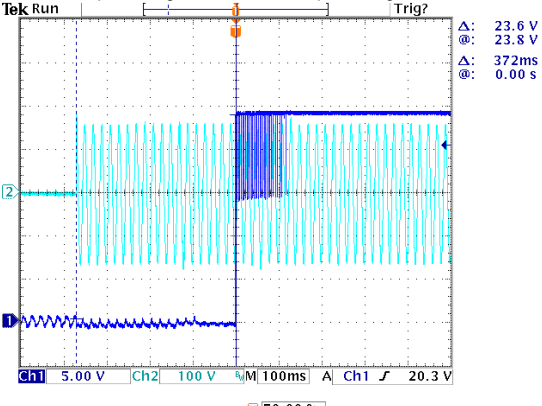
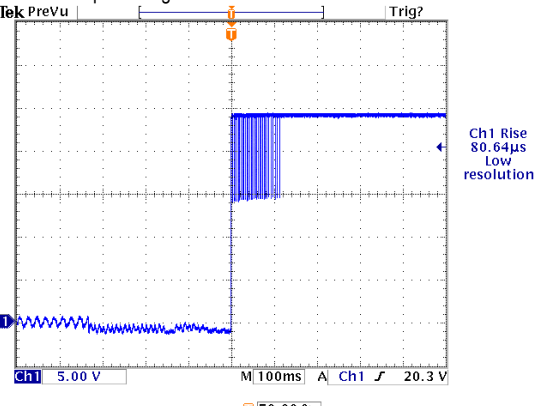
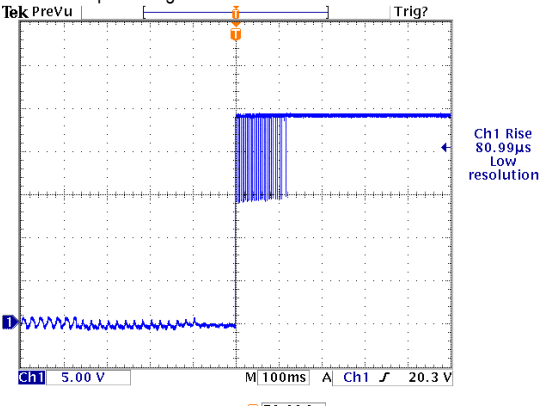
E.M.C. Test

■ RELIABILITY TEST

Environment Test

DESIGN VERIFY TEST

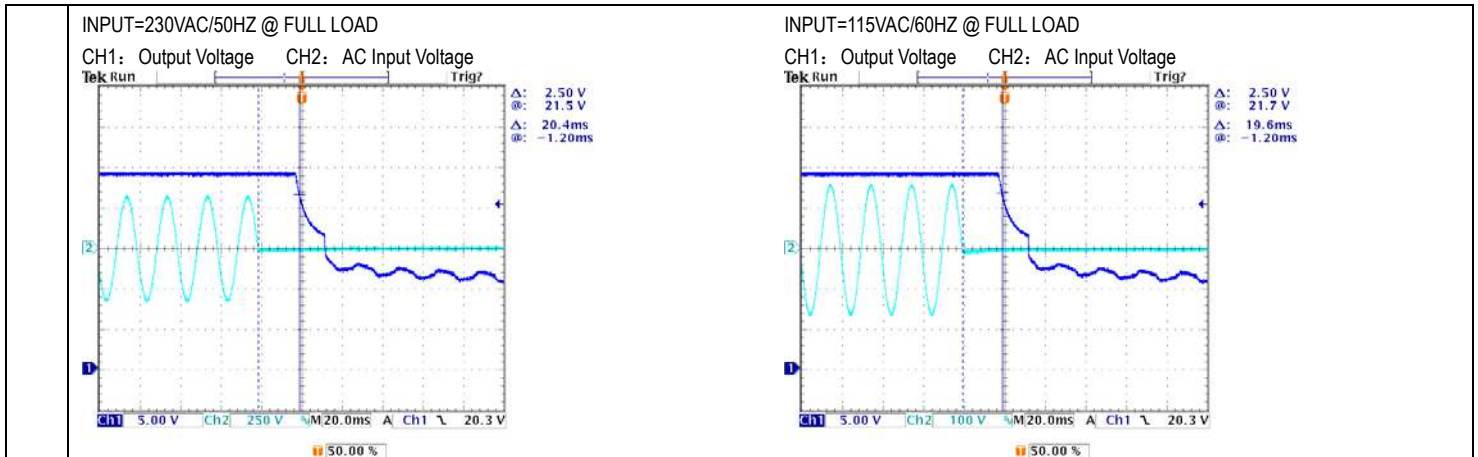
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM FREQUENCY	1.47KHz	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	1.479KHz
2	OVER/UNDERSHOOT TEST	<± 5 %	I/P: 230VAC O/P: FULL LOAD Ta: 25°C	<5 %
3	SET UP TIME(Max)	230VAC/ 500ms 115VAC/ 500ms	I/P: 230 VAC I/P: 115 VAC O/P: FULL LOAD Ta: 25°C	230VAC/ 334ms 115VAC/ 372ms
INPUT=230VAC/50HZ @ FULL LOAD CH1: Output Voltage CH2: AC Input Voltage  Δ: 23.5 V @: 23.8 V Δ: 334ms @: 0.00 s INPUT=115VAC/60HZ @ FULL LOAD CH1: Output Voltage CH2: AC Input Voltage  Δ: 23.6 V @: 23.8 V Δ: 372ms @: 0.00 s				
4	RISE TIME (Max)	230VAC/ 80ms 115VAC/ 80ms	I/P: 230 VAC I/P: 115 VAC O/P: FULL LOAD Ta: 25°C	230VAC/0.081ms 115VAC/0.081ms
INPUT=230VAC/50HZ @ FULL LOAD CH1: Output Voltage  Ch1 Rise 80.64μs Low resolution INPUT=115VAC/60HZ @ FULL LOAD CH1: Output Voltage  Ch1 Rise 80.99μs Low resolution				
5	HOLD UP TIME(Typ)	230VAC/ 16ms 115VAC/ 16ms	I/P: 230 VAC I/P: 115 VAC O/P: FULL LOAD Ta: 25°C	230VAC/20.4ms 115VAC/19.6ms



120W PWM Output LED Driver

PWM-120 series

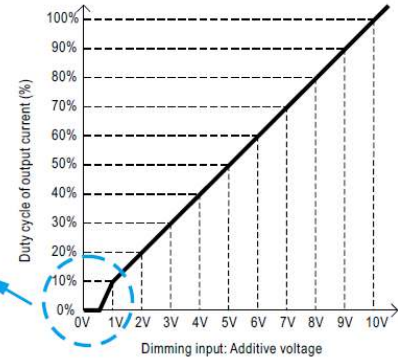
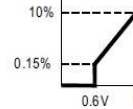
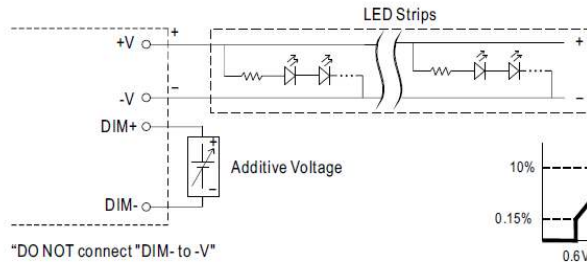


6 DIMMING TEST(for Blank-Type)

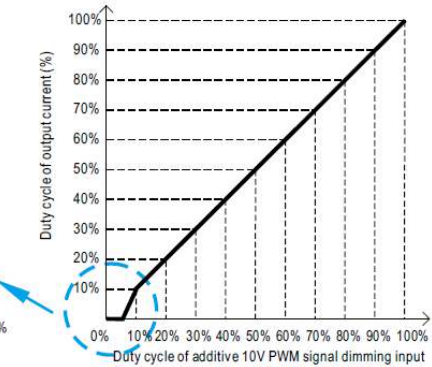
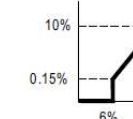
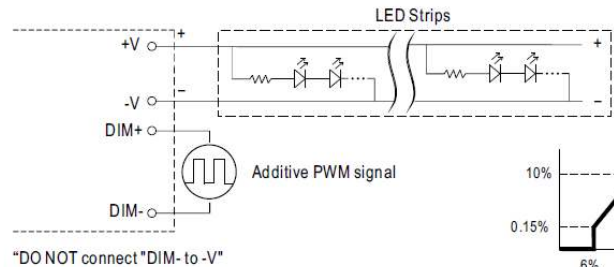
※ 3 in 1 dimming function (for Blank-Type)

- Apply one of the three methodologies between DIM+ and DIM-: 0 ~ 10VDC, or 10V PWM signal or resistance.
- Dimming source current from power supply: 100 μ A (typ.)

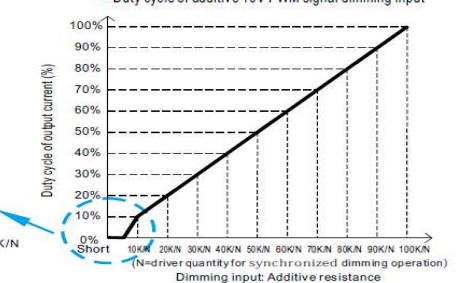
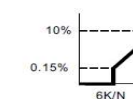
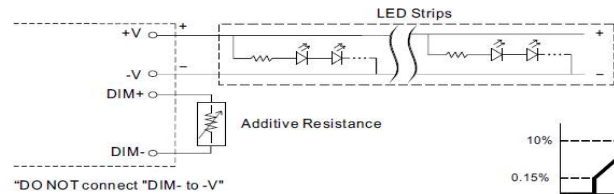
◎ Applying additive 0 ~ 10VDC



◎ Applying additive 10V PWM signal (frequency range 100Hz ~ 3KHz):



◎ Applying additive resistance:



Note : 1. Min. duty cycle of output current is about 0.15%, and the dimming input is about 6K Ω or 0.6VDC, or 10V PWM signal with 6% duty cycle.
2. The duty cycle of output current could drop down to 0% when dimming input is less than 6K Ω or less than 0.6VDC, or 10V PWM signal with duty cycle less than 6%.

I/P: 230 VAC; O/P: DIMMING TEST; Ta: 25 $^{\circ}$ C

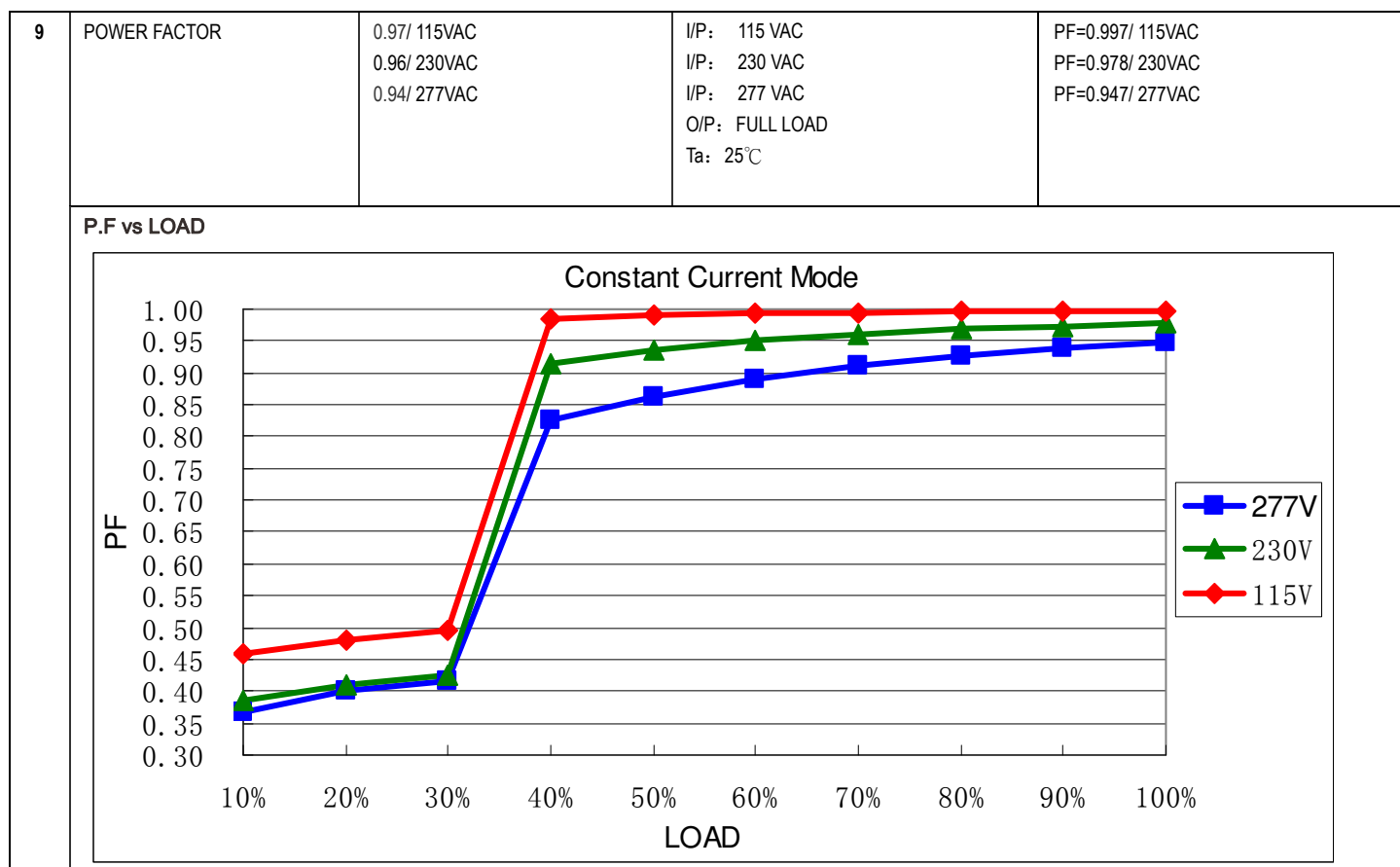
1	Resistance value	Short	10K	20K	30K	40K	50K	60K	70K	80K	90K	100K	OPEN
	Output Current	0	0.525	1.039	1.532	2.053	2.557	3.042	3.557	4.031	4.547	5.028	5.028
	Output Current duty	0%	10.49 %	20.77 %	30.64 %	41.07 %	51.13 %	60.83 %	71.15 %	80.63 %	90.94 %	100.56 %	100.56 %
2	Dimming value	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN
	Output Current	0	0.505	0.999	1.476	1.981	2.490	2.981	3.503	4.014	4.522	5.028	5.028
	Output Current duty	0%	10.10 %	19.97 %	29.52 %	39.61 %	49.80 %	59.63 %	70.06 %	80.27 %	90.45 %	100.56 %	100.56 %
3	Duty value	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN
	Output Current	0	0.504	0.967	1.461	1.957	2.458	2.941	3.456	3.979	4.481	4.939	5.028
	Output Current duty	0%	10.08 %	19.35 %	29.22 %	39.13 %	49.16 %	58.81 %	69.13 %	79.58 %	89.63 %	98.79 %	100.56 %

7	DALI DIMMING OPERATION (primary side; for DA-Type)	※DALI Interface ·Apply DALI signal between DA+ and DA-. ·DALI protocol comprises 16 groups and 64 addresses. ·First step is fixed at 6% of output. I/P: 230 VAC O/P: DIMMING TEST Ta: 25°C TEST RESULT: OK
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INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	90VAC~305VAC	I/P: TESTING O/P: FULL LOAD Ta: 25°C	87V~305V
			I/P: (1)LOW-LINE-3V=87 V HIGH-LINE+10V=315 V O/P: FULL/NO LOAD ON: 30 Sec OFF: 30 Sec 10MIN (2)230VAC ON: 0.5 Sec OFF: 0.5 Sec 20MIN	TEST: OK
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P: 90 VAC ~305 VAC O/P: FULL~NO LOAD Ta: 25°C	TEST: OK
3	AC CURRENT	1.3A/115VAC 0.65A/230VAC 0.55A/277VAC	I/P: 115 VAC I/P: 230 VAC I/P: 277 VAC O/P: FULL LOAD Ta: 25°C	I=1.156A/ 115VAC I=0.587A/ 230VAC I=0.502A/ 277VAC
4	LEAKAGE CURRENT	< 0.25mA / 277VAC	I/P: 277 VAC O/P: NO LOAD Ta: 25°C	L-FG: 0.003 mA N-FG: 0.003 mA
5	NO LOAD/STANDBY POWER CONSUMPTION	< 0.5W	I/P: 230VAC O/P: NO LOAD Ta: 25°C	0.321W
6	TOTAL HARMONIC DISTORTION	THD<20% (@load≥60%/115VAC, 230VAC; @load≥75%/277VAC)	I/P: 115 VAC/60% LOAD I/P: 230 VAC/60% LOAD I/P: 277 VAC/75% LOAD Ta: 25°C	THD: 6.46 %@60% load /115VAC THD: 16.97 %@60% load /230VAC THD: 18.36 %@75% load /277VAC

7	INRUSH CURRENT(Typ)	60A/230VAC Twidth =520 us measured at 50% Ipeak COLD START	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	I=47.4A/ 230VAC Twidth =508us
INPUT=230VAC/50HZ @ FULL LOAD CH2: Input current CH1: AC Input Voltage Tek Run				



PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER LOAD PROTECTION	108 %~ 120 %	I/P: 230VAC O/P: TESTING Ta: 25°C	113.65%/ 230VAC Hiccup mode, recovers automatically after fault condition is removed
2	OVER VOLTAGE PROTECTION	28V~34V	I/P: 230VAC O/P: NO LOAD Ta: 25°C	31.30V/ 230VAC Shut down o/p voltage, re-power on to recover
3	OVER TEMPERATURE PROTECTION	NO DAMAGE	I/P: 230 VAC O/P: FULL LOAD	O.T.P. Active Shut down o/p voltage, re-power on to recover
4	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 295VAC O/P: FULL LOAD Ta: 25°C	NO DAMAGE Shut down o/p voltage, re-power on to recover

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Transistor (D to S) or (C to E) Peak Voltage	Q 2 Rated 730V/10A	I/P: High-Line +3V =308V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25℃	(1) 724V (2) 710V (3) 680V
2	Diode Peak Voltage	Q101 Rated 120V/56A	I/P: High-Line +3V =308V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25℃	(1) 101V (2) 103V (3) 99.6V
3	Input Capacitor Voltage	C5 Rated 100u/ 450V	I/P: High-Line +3V =308 V O/P: (1) Full Load input on/off (2) NO load input on /Off (3) Full Load /NO load Change Ta: 25℃	(1) 442V (2) 440V (3) 442V
4	Control IC Voltage Test	U1 Rated 28V	I/P: High-Line +3V =308 V O/P: (1) Full Load input on/off (2) NO load input on /Off (3) Full Load /NO load Change Ta: 25℃	(1) 17.1V (2) 17.1V (3) 17.1V
5	PFC Transistor (D to S) or (C to E) Peak Voltage	Q 1 Rated 600V/15A	I/P: High-Line +3V =308V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25℃	(1) 479V (2) 456V (3) 478V



SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P: 3.75KVAC/min	I/P-O/P: 4.2KVAC/min Ta: 25°C	I/P-O/P: 1.895mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P: 500VDC>100MΩ	I/P-O/P: 500VDC Ta: 25°C	I/P-O/P: >9999MΩ

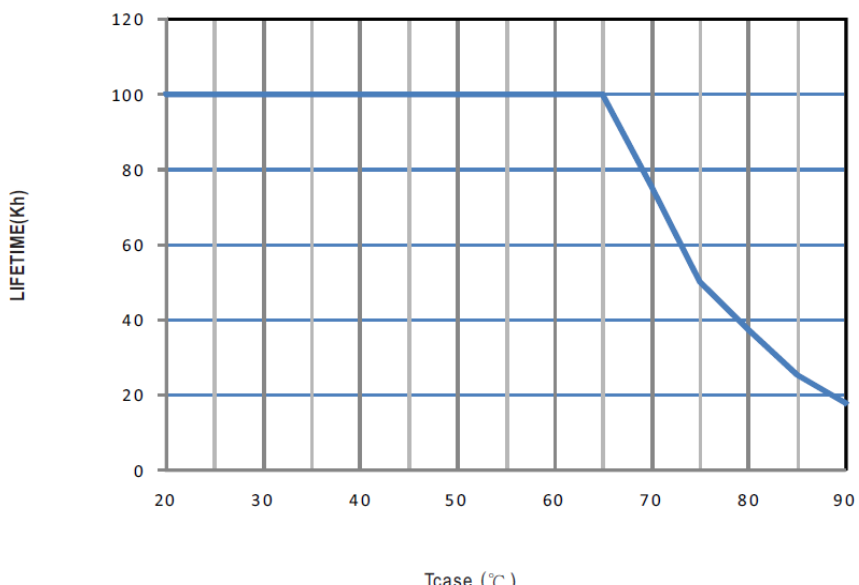
E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	HARMONIC	EN61000-3-2 CLASS C	I/P: 230VAC/50HZ O/P: 60%/FULL LOAD Ta:25°C	PASS
2	CONDUCTION	EN55015	I/P: 230 VAC (50HZ) O/P: FULL LOAD Ta: 25°C	PASS Test by certified Lab
3	RADIATION	EN55015	I/P: 230 VAC (50HZ) O/P: FULL LOAD Ta: 25°C	PASS Test by certified Lab
4	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR: 8KV Contact: 4KV	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
5	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT: 1KV	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
6	SURGE	EN61000-4-5 INDUSTRY L-N: 2KV	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS
7	Test by certified Lab & Test Report Prepare			

RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																																
1	TEMPERATURE RISE TEST	MODEL: PWM-120-36 1. ROOM AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: FULL LOAD Ta= 30.5℃ 2. HIGH AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: FULL LOAD Ta= 46.2℃ <table><tr><th>NO</th><th>Position</th><th>ROOM AMBIENT Ta= 30.5 ℃</th><th>HIGH AMBIENT Ta=46.2 ℃</th></tr><tr><td>1</td><td>C110</td><td>59.8℃</td><td>73.1℃</td></tr><tr><td>2</td><td>Q105</td><td>63.2℃</td><td>76.4℃</td></tr><tr><td>3</td><td>C5</td><td>72.3℃</td><td>85.9℃</td></tr><tr><td>4</td><td>C11</td><td>73.1℃</td><td>87.5℃</td></tr><tr><td>5</td><td>C105</td><td>68.9℃</td><td>82.0℃</td></tr><tr><td>6</td><td>T1</td><td>77.8℃</td><td>95.8℃</td></tr><tr><td>7</td><td>RTH2</td><td>75.6℃</td><td>88.7℃</td></tr><tr><td>8</td><td>Q1</td><td>75.6℃</td><td>90.4℃</td></tr><tr><td>9</td><td>Q2</td><td>81.5℃</td><td>97.2℃</td></tr><tr><td>10</td><td>Q101</td><td>73.8℃</td><td>86.8℃</td></tr><tr><td>11</td><td>RTH1</td><td>60.3℃</td><td>72.0℃</td></tr><tr><td>12</td><td>D10</td><td>85.8℃</td><td>101.8℃</td></tr><tr><td>13</td><td>C45</td><td>69.7℃</td><td>83.2℃</td></tr><tr><td>14</td><td>U1</td><td>68.5℃</td><td>82.3℃</td></tr><tr><td>15</td><td>R7</td><td>82.9℃</td><td>97.8℃</td></tr><tr><td>16</td><td>C15</td><td>85.5℃</td><td>99.9℃</td></tr><tr><td>17</td><td>C12</td><td>75.7℃</td><td>90.1℃</td></tr><tr><td>18</td><td>RTH3</td><td>67.6℃</td><td>81.1℃</td></tr><tr><td>19</td><td>TC</td><td>62.5℃</td><td>76.6℃</td></tr></table>			NO	Position	ROOM AMBIENT Ta= 30.5 ℃	HIGH AMBIENT Ta=46.2 ℃	1	C110	59.8℃	73.1℃	2	Q105	63.2℃	76.4℃	3	C5	72.3℃	85.9℃	4	C11	73.1℃	87.5℃	5	C105	68.9℃	82.0℃	6	T1	77.8℃	95.8℃	7	RTH2	75.6℃	88.7℃	8	Q1	75.6℃	90.4℃	9	Q2	81.5℃	97.2℃	10	Q101	73.8℃	86.8℃	11	RTH1	60.3℃	72.0℃	12	D10	85.8℃	101.8℃	13	C45	69.7℃	83.2℃	14	U1	68.5℃	82.3℃	15	R7	82.9℃	97.8℃	16	C15	85.5℃	99.9℃	17	C12	75.7℃	90.1℃	18	RTH3	67.6℃	81.1℃	19	TC	62.5℃	76.6℃
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19	TC	62.5℃	76.6℃																																																																																	
2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P: 305VAC/100VAC O/P: FULL LOAD Ta= -45℃ / -30℃	TEST: OK																																																																																
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 45 ℃ NO DAMAGE	I/P: 315VAC O/P: FULL LOAD Ta=45 ℃ HUMIDITY= 95% R.H	TEST: OK																																																																																
4	TEMPERATURE COEFFICIENT	±0.03%/℃ (0~50℃)	I/P: 230 VAC O/P: FULL LOAD	±0.003%/℃ (0~50℃)																																																																																
5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature: -45℃ ~ +85℃ 2. Temperature change rate : 25℃ / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 5 CYCLE 5. Input/Output condition: STATIC		TEST: OK																																																																																

6	THERMAL SHOCK TEST	1. Thermal shock Temperature: -45℃ ~ +50℃ 2. Temperature change rate : 25℃ / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 10 CYCLE 5. Input/Output condition: 230VAC/Full Load AC ON/OFF TEST turn on 58 sec, turn off 2 sec;	TEST: OK																								
7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform: Sine Wave (2) Frequency: 10~500Hz (3) Sweep Time: 12min/sweep cycle (4) Acceleration: 5G (5) Test Time: 72min in each axis (X.Y.Z) (6) Ta: 25℃	TEST: OK																								
8	CAPACITOR LIFE CYCLE	PWM-120-36: SUPPOSE C105 IS THE MOST CRITICAL COMPONENT (1) I/P: 230VAC O/P: FULL LOAD Ta= 25 ℃ LIFE TIME (2) I/P: 230VAC O/P: FULL LOAD Ta= 45 ℃ LIFE TIME (3) I/P: 230VAC O/P: 75% LOAD Ta= 45 ℃ LIFE TIME (4) I/P: 230VAC O/P: 50% LOAD Ta= 45 ℃ LIFE TIME	(1) 216348 HRS (2) 45738 HRS (3) 60016 HRS (4) 94864 HRS																								
9	MTBF	Conducted by Parts Stress Analysis Prediction 860.4K hrs min. Telcordia SR-332 (Bellcore) 228.7K hrs min. MIL-HDBK-217F (25℃)																									
10	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure (Expected Life): Above 50000 hours @ TC 75℃ <table><caption>Data points for Lifetime vs Tcase</caption><thead><tr><th>Tcase (°C)</th><th>Lifetime (kh)</th></tr></thead><tbody><tr><td>20</td><td>100</td></tr><tr><td>30</td><td>100</td></tr><tr><td>40</td><td>100</td></tr><tr><td>50</td><td>100</td></tr><tr><td>60</td><td>100</td></tr><tr><td>65</td><td>100</td></tr><tr><td>70</td><td>80</td></tr><tr><td>75</td><td>50</td></tr><tr><td>80</td><td>35</td></tr><tr><td>85</td><td>25</td></tr><tr><td>90</td><td>20</td></tr></tbody></table>		Tcase (°C)	Lifetime (kh)	20	100	30	100	40	100	50	100	60	100	65	100	70	80	75	50	80	35	85	25	90	20
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75	50																										
80	35																										
85	25																										
90	20																										

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	ZHANGZJ/ZHUOKB	SKY	LIUWY