



Test Report: DRDN20-12

20A DIN Rail Type Redundancy Module

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Control Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

E.M.C. Test

■ RELIABILITY TEST

ENVIRONMENT TEST

DESIGN VERIFY TEST

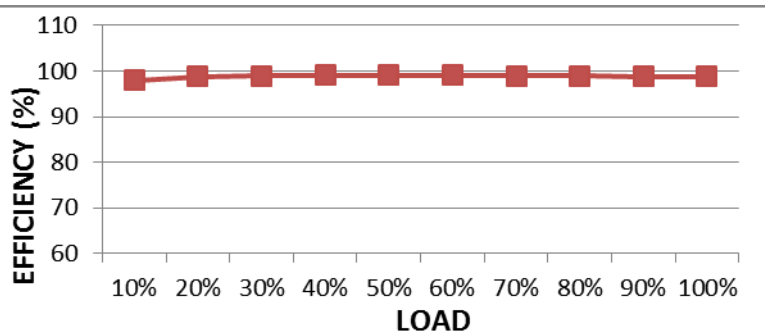
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RATED CURRENT	0~20A CONTINUOUS	I/P : 12VDC O/P : FULL LOAD Ta : 25℃	OK
2	PEAK CURRENT	30A 5Sec NO DAMAGE	I/P: 12VDC O/P : 30A Ta:25℃	OK
3	CAPACITANCE	320uF	I/P : 12VDC O/P : 320uF Ta : 25℃	OK
4	STANDBY POWER LOSSES (Typ.)	1.5W	I/P : 12VDC O/P : NO LOAD Ta : 25℃	0.23W

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	9VDC~14VDC	I/P:TESTING O/P:FULL LOAD Ta:25℃	8.54VDC~14.73
2	RATED CURRENT	0~10Ax2 input, 0~20Ax1 input Continuous	I/P : 12VDC O/P: 0~10Ax2 input, 0~20Ax1 Ta:25℃	OK
3	VOLTAGE DROP (Vin-Vout) (max.)	0.25V	I/P : 12VDC O/P : FULL LOAD Ta : 25℃	0.18V
4	PEAK CURRENT	0~15Ax2 input, 0~30Ax1 5Sec NO DAMAGE	I/P: 12VDC O/P : 30A Ta:25℃	OK
5	INPUT REVERSE CURRENT (max.)	1mA	I/P : 12VDC O/P : FULL LOAD Ta : 25℃	4uA
6	INPUT REVERSE VOLTAGE (max.)	40Vdc NO DAMAGE	I/P : 40VDC O/P : FULL LOAD Ta : 25℃	OK
7	EFFICIENCY(Typ.)	98%	I/P:12VDC O/P:FULL LOAD Ta:25℃	98.853 %

EFFICIENCY vs LOAD



PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER LOAD PROTECTION	<30A No damage 5 sec (max)	I/P:12VDC O/P:TESTING Ta:25°C	NO DAMAGE
2	SHORT PROTECTION	<30A No damage 5 sec (max)	I/P: 14VDC O/P: FULL LOAD Ta:25°C	NO DAMAGE

CONTROL FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RELAY	30VDC/1A RESISTIVE LOAD	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST : OK
2	REDUNDANCY	For 1+1 redundancy,and support N+1 redundancy	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST :OK
3	BOTH INPUTS VOLTANG ALARM	<8.5V OR >14.7V (±5%)	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST :OK
4	LED STATUS DISPLAY	GREEN LED OK	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST :OK

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	Transistor Peak Voltage	Q1 VGS Rated : ±20V Q3 VGS Rated : ±20V	I/P:14VDC DC ON/OFF O/P:FULL LOAD Ta:25°C	Q1 VGS:11.8V Q3 VGS:12.4V

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P/O/P-FG: 0.5KVAC/min I/P/FG-RELAY :0.5KVAC/min FG-RELAY:0.5KVAC/min	I/P/O/P-FG: 0.6 KVAC/min I/P/FG-RELAY: 0.6 KVAC/min O/P-FG:0.6 KVAC/min Ta:25°C	I/P/O/P-FG:7.39mA I/P/FG-RELAY:0.279mA FG-RELAY:0.267mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P/O/P-FG:500VDC>100MΩ I/P/FG-RELAY: 500VDC>100MΩ FG-RELAY:500VDC>100MΩ	I/P/O/P-FG: 500 VDC I/P/FG-RELAY: 500 VDC FG-RELAY: 500 VDC Ta:25°C	I/P/O/P-FG: 9999MΩ I/P/FG-RELAY: 9999MΩ FG-RELAY: 9999MΩ NO DAMAGE
3	GROUNDING CONTINUITY	FG(PE) TO CHASSIS OR TRACE < 100 mΩ	40A / 2min Ta:25°C	4 mΩ

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	CONDUCTION	☒ EN55032 CLASS B	I/P : 12VDC O/P : FULL/50% LOAD Ta : 25°C	PASS Test by certified Lab
2	RADIATION	☒ EN55032 CLASS B	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	PASS Test by certified Lab
3	E.S.D	EN61000-4-2 ☒ Din rail Model : AIR: 15KV / Contact: 8KV	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	☒ CRITERIA A ☐ CRITERIA B
4	E.F.T	EN61000-4-4 ☒ INDUSTRY INPUT : 2KV	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	☒ CRITERIA A ☐ CRITERIA B
5	SURGE	IEC61000-4-5 ☒ LIGHT INDUSTRY L-N : 1KV L,N-PE : 2KV	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	☒ CRITERIA A ☐ CRITERIA B
6	Test by certified Lab & Test Report Prepare Any contradictions of the test results, please refer to the latest EMC test report			

RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																
1	TEMPERATURE RISE TEST	MODEL : DRDN20-12 1. ROOM AMBIENT BURN-IN : 2 HRS I/P : 12VDC O/P : FULL LOAD Ta= 26.2 °C 2. HIGH AMBIENT BURN-IN : 2 HRS I/P : 12VDC O/P : FULL LOAD Ta= 59.6 °C <table><tr><th>NO</th><th>Position</th><th>ROOM AMBIENT Ta= 26.2 °C</th><th>HIGH AMBIENT Ta= 59.6 °C</th></tr><tr><td>1</td><td>ZNR1</td><td>47.8°C</td><td>85.2°C</td></tr><tr><td>2</td><td>Q1</td><td>50.1°C</td><td>89.4°C</td></tr><tr><td>3</td><td>Q3</td><td>70.4°C</td><td>115.8°C</td></tr><tr><td>4</td><td>C17</td><td>56.8°C</td><td>95.2°C</td></tr><tr><td>5</td><td>ZNR2</td><td>52.3°C</td><td>89.9°C</td></tr><tr><td>6</td><td>C15</td><td>48.0°C</td><td>86.0°C</td></tr><tr><td>7</td><td>C16</td><td>51.5°C</td><td>90.6°C</td></tr><tr><td>8</td><td>RY1</td><td>43.1°C</td><td>80.5°C</td></tr><tr><td>9</td><td>RY2</td><td>51.9°C</td><td>89.3°C</td></tr><tr><td>10</td><td>PCB</td><td>66.5°C</td><td>107.8°C</td></tr><tr><td>11</td><td>U4</td><td>46.0°C</td><td>83.6°C</td></tr><tr><td>12</td><td>D3</td><td>45.5°C</td><td>82.8°C</td></tr><tr><td>13</td><td>R54</td><td>43.1°C</td><td>80.4°C</td></tr><tr><td>14</td><td>R56</td><td>45.1°C</td><td>82.6°C</td></tr><tr><td>15</td><td>C4</td><td>52.1°C</td><td>90.3°C</td></tr></table>			NO	Position	ROOM AMBIENT Ta= 26.2 °C	HIGH AMBIENT Ta= 59.6 °C	1	ZNR1	47.8°C	85.2°C	2	Q1	50.1°C	89.4°C	3	Q3	70.4°C	115.8°C	4	C17	56.8°C	95.2°C	5	ZNR2	52.3°C	89.9°C	6	C15	48.0°C	86.0°C	7	C16	51.5°C	90.6°C	8	RY1	43.1°C	80.5°C	9	RY2	51.9°C	89.3°C	10	PCB	66.5°C	107.8°C	11	U4	46.0°C	83.6°C	12	D3	45.5°C	82.8°C	13	R54	43.1°C	80.4°C	14	R56	45.1°C	82.6°C	15	C4	52.1°C	90.3°C
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2	OVER LOAD BURN-IN TEST	NO DAMAGE 1 HOUR (MIN)	I/P : 12 VDC O/P : 115% LOAD Ta : 25°C	TEST : OK																																																																

3	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 9VDC/14VDC O/P : 100 % LOAD Ta= -45°C	TEST : OK
4	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60 °C /95 %R.H NO DAMAGE	I/P : 14 VDC O/P : FULL LOAD Ta= 60 °C HUMIDITY= 95 %R.H	TEST : OK
5	TEMPERATURE COEFFICIENT	± 0.03%/°C (0~60°C)	I/P : 12 VDC O/P : FULL LOAD	± 0.01%/°C (0~60°C)
6	STORAGE TEMPERATURE TEST	-40~85°C	1. Thermal shock Temperature : -45°C~+90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : STATIC	
7	THERMAL SHOCK TEST	-40~60°C	1. Thermal shock Temperature : -45°C~+65°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 16 CYCLE 5. Input/Output condition : 15cycle:12VDC/ FULL LOAD AC ON 3sec/AC OFF 1sec TEST 1cycle:12VDC/ FULL LOAD Burn In Test	
8	VIBRATION TEST	10 ~ 500Hz, 5G 10min./1cycle, 60min. each along X, Y, Z axes	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 12min/sweep cycle (4) Acceleration : 6G (5) Test Time : 180min in each axis (X.Y.Z) (6) Ta : 25°C	
9	CAPACITOR LIFE CYCLE	SUPPOSE C16 IS THE MOST CRITICAL COMPONENT (1) I/P : 12VDC O/P : FULL LOAD Ta= 25 °C LIFE TIME (2) I/P : 12VDC O/P : FULL LOAD Ta= 60 °C LIFE TIME (3) I/P : 12VDC O/P : 75% LOAD Ta= 60 °C LIFE TIME (4) I/P : 12VDC O/P : 50% LOAD Ta= 60 °C LIFE TIME		(1) 310265 HRS (2) 18473 HRS (3) 46121 HRS (4) 86065 HRS
10	MTBF	Conducted by Parts Stress Analysis Prediction 611.82K hrs min. Telcordia SR-332 (Bellcore) ; 353.67K hrs min. MIL-HDBK-217F (25°C)		
11	Ongoing Reliability Test	I/P : 12VDC O/P : FULL LOAD TA=50°C Demonstration Mean Time Between Failure : 30000 hours		

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	LIUTT		Wangdz

2018.4.30 GP-A50-F010