



Test Report: DRDN40-12

40A DIN Rail Type Redundancy Module

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Control Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

E.M.C. Test

■ RELIABILITY TEST

ENVIRONMENT TEST

DESIGN VERIFY TEST

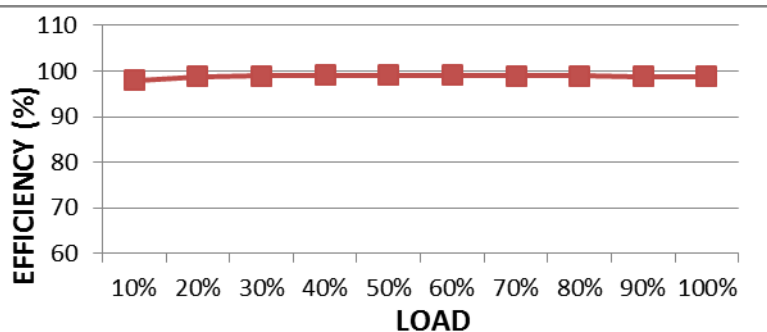
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RATED CURRENT	0~40A CONTINUOUS	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	OK
2	PEAK CURRENT	60A 5Sec NO DAMAGE	I/P: 12VDC O/P : 60A Ta:25°C	OK
3	CAPACITANCE	320uF	I/P : 12VDC O/P : 320uF Ta : 25°C	OK
4	STANDBY POWER LOSSES (Typ.)	1.5W	I/P : 12VDC O/P : NO LOAD Ta : 25°C	0.22W

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	9VDC~14VDC	I/P:TESTING O/P:FULL LOAD Ta:25°C	8.62VDC~14.58VDC
2	RATED CURRENT	0~20Ax2 input, 0~40Ax1 input Continuous	I/P : 12VDC O/P: 0~20Ax2 input, 0~40Ax1 Ta:25°C	OK
3	VOLTAGE DROP (Vin-Vout) (max.)	0.3V	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	0.24V
4	PEAK CURRENT	0~30Ax2 input, 0~60Ax1 5Sec NO DAMAGE	I/P: 12VDC O/P : 60A Ta:25°C	OK
5	INPUT REVERSE CURRENT (max.)	1mA	I/P : 40VDC O/P : FULL LOAD Ta : 25°C	2.14uA
6	INPUT REVERSE VOLTAGE (max.)	40Vdc NO DAMAGE	I/P : 40VDC O/P : FULL LOAD Ta : 25°C	OK
7	EFFICIENCY(Typ.)	98%	I/P:12VDC O/P:FULL LOAD Ta:25°C	98.48 %

EFFICIENCY vs LOAD



PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER LOAD PROTECTION	<60A No damage 5 sec (max)	I/P:12VDC O/P:TESTING Ta:25°C	NO DAMAGE
2	SHORT PROTECTION	<60A No damage 5 sec (max)	I/P: 14VDC O/P: FULL LOAD Ta:25°C	NO DAMAGE

CONTROL FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RELAY	30VDC/1A RESISTIVE LOAD	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST : OK
2	REDUNDANCY	For 1+1 redundancy,and support N+1 redundancy	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST :OK
3	BOTH INPUTS VOLTANG ALARM	<8.5V OR >14.7V (±5%)	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST :OK
4	LED STATUS DISPLAY	GREEN LED OK	I/P:12VDC O/P:FULL LOAD Ta:25°C	TEST :OK

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	Transistor Peak Voltage	Q1 VGS Rated : ±20V Q3 VGS Rated : ±20V	I/P:14VDC DC ON/OFF O/P:FULL LOAD Ta:25°C	Q1 VGS:12.0V Q3 VGS:12.0V

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P/O/P-FG: 0.5KVAC/min I/P/FG-RELAY :0.5KVAC/min FG-RELAY:0.5KVAC/min	I/P/O/P-FG: 0.6 KVAC/min I/P/FG-RELAY: 0.6 KVAC/min O/P-FG:0.6 KVAC/min Ta:25°C	I/P/O/P-FG:7.44mA I/P/FG-RELAY:0.285mA FG-RELAY:0.272mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P/O/P-FG:500VDC>100MΩ I/P/FG-RELAY: 500VDC>100MΩ FG-RELAY:500VDC>100MΩ	I/P/O/P-FG: 500 VDC I/P/FG-RELAY: 500 VDC FG-RELAY: 500 VDC Ta:25°C	I/P/O/P-FG: 9999MΩ I/P/FG-RELAY: 9999MΩ FG-RELAY:8739MΩ NO DAMAGE
3	GROUNDING CONTINUITY	FG(PE) TO CHASSIS OR TRACE < 100 mΩ	40A / 2min Ta:25°C	7 mΩ

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	CONDUCTION	☒ EN55032 CLASS B	I/P : 12VDC O/P : FULL/50% LOAD Ta : 25°C	PASS Test by certified Lab
2	RADIATION	☒ EN55032 CLASS B	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	PASS Test by certified Lab
3	E.S.D	EN61000-4-2 ☒ Din rail Model : AIR: 15KV / Contact: 8KV	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	☒ CRITERIA A ☐ CRITERIA B
4	E.F.T	EN61000-4-4 ☒ INDUSTRY INPUT : 2KV	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	☒ CRITERIA A ☐ CRITERIA B
5	SURGE	IEC61000-4-5 ☒ LIGHT INDUSTRY L-N : 1KV L,N-PE : 2KV	I/P : 12VDC O/P : FULL LOAD Ta : 25°C	☒ CRITERIA A ☐ CRITERIA B
6	Test by certified Lab & Test Report Prepare Any contradictions of the test results, please refer to the latest EMC test report			

■ RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																				
1	TEMPERATURE RISE TEST	MODEL : DRDN40-24 1. ROOM AMBIENT BURN-IN : 2 HRS I/P : 24VDC O/P : FULL LOAD Ta= 22.7 °C 2. HIGH AMBIENT BURN-IN : 2 HRS I/P : 24VDC O/P : FULL LOAD Ta= 59.3 °C <table><thead><tr><th>NO</th><th>Position</th><th>ROOM AMBIENT Ta= 22.7 °C</th><th>HIGH AMBIENT Ta= 59.3 °C</th></tr></thead><tbody><tr><td>1</td><td>ZNR2</td><td>58.0°C</td><td>98.1°C</td></tr><tr><td>2</td><td>Q1</td><td>55.2°C</td><td>95.3°C</td></tr><tr><td>3</td><td>Q2</td><td>56.2°C</td><td>96.7°C</td></tr><tr><td></td><td>Q3</td><td>70.7°C</td><td>114.0°C</td></tr><tr><td></td><td>Q4</td><td>65.1°C</td><td>108.2°C</td></tr><tr><td>4</td><td>C17</td><td>63.8°C</td><td>103.4°C</td></tr><tr><td>5</td><td>C15</td><td>43.3°C</td><td>84.0°C</td></tr><tr><td>6</td><td>C16</td><td>40.4°C</td><td>81.0°C</td></tr><tr><td>7</td><td>RY1</td><td>50.5°C</td><td>88.5°C</td></tr><tr><td>8</td><td>RY2</td><td>51.8°C</td><td>89.2°C</td></tr><tr><td>9</td><td>PCB</td><td>71.7°C</td><td>113.7°C</td></tr><tr><td>10</td><td>U4</td><td>47.7°C</td><td>86.2°C</td></tr><tr><td>11</td><td>D3</td><td>51.9°C</td><td>90.5°C</td></tr><tr><td>12</td><td>R54</td><td>49.6°C</td><td>87.9°C</td></tr><tr><td>13</td><td>R56</td><td>53.7°C</td><td>91.9°C</td></tr><tr><td>14</td><td>R48</td><td>52.0°C</td><td>90.9°C</td></tr></tbody></table>			NO	Position	ROOM AMBIENT Ta= 22.7 °C	HIGH AMBIENT Ta= 59.3 °C	1	ZNR2	58.0°C	98.1°C	2	Q1	55.2°C	95.3°C	3	Q2	56.2°C	96.7°C		Q3	70.7°C	114.0°C		Q4	65.1°C	108.2°C	4	C17	63.8°C	103.4°C	5	C15	43.3°C	84.0°C	6	C16	40.4°C	81.0°C	7	RY1	50.5°C	88.5°C	8	RY2	51.8°C	89.2°C	9	PCB	71.7°C	113.7°C	10	U4	47.7°C	86.2°C	11	D3	51.9°C	90.5°C	12	R54	49.6°C	87.9°C	13	R56	53.7°C	91.9°C	14	R48	52.0°C	90.9°C
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2	OVER LOAD BURN-IN TEST	NO DAMAGE 1 HOUR (MIN)	I/P : 24 VDC O/P : 115% LOAD Ta : 25°C	TEST : OK																																																																				

3	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 19VDC/29VDC O/P : 100 % LOAD Ta= -45°C	TEST : OK
4	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60 °C /95 %R.H NO DAMAGE	I/P : 29 VDC O/P : FULL LOAD Ta= 60 °C HUMIDITY= 95 %R.H	TEST : OK
5	TEMPERATURE COEFFICIENT	± 0.03%/°C (0~60°C)	I/P : 24 VDC O/P : FULL LOAD	± 0.018%/°C (0~60°C)
6	STORAGE TEMPERATURE TEST	-40~85°C	1. Thermal shock Temperature : -45°C~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : STATIC	
7	THERMAL SHOCK TEST	-40~60°C	1. Thermal shock Temperature : -45°C~ +65°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 16 CYCLE 5. Input/Output condition : 15cycle:24VDC/ FULL LOAD AC ON 3sec/AC OFF 1sec TEST 1cycle:24VDC/ FULL LOAD Burn In Test	
8	VIBRATION TEST	10 ~ 500Hz, 5G 10min./1cycle, 60min. each along X, Y, Z axes	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 12min/sweep cycle (4) Acceleration : 6G (5) Test Time : 180min in each axis (X.Y.Z) (6) Ta : 25°C	
9	CAPACITOR LIFE CYCLE	SUPPOSE C15 IS THE MOST CRITICAL COMPONENT (1) I/P : 24VDC O/P : FULL LOAD Ta= 25 °C LIFE TIME (2) I/P : 24VDC O/P : FULL LOAD Ta= 60 °C LIFE TIME (3) I/P : 24VDC O/P : 75% LOAD Ta= 60 °C LIFE TIME (4) I/P : 24VDC O/P : 50% LOAD Ta= 60 °C LIFE TIME		(1) 429750 HRS (2) 28588 HRS (3) 56782 HRS (4) 85471 HRS
10	MTBF	Conducted by Parts Stress Analysis Prediction 557.52K hrs min. Telcordia SR-332 (Bellcore) ; 277.24K hrs min. MIL-HDBK-217F (25°C)		
11	Ongoing Reliability Test	I/P : 24VDC O/P : FULL LOAD TA=50°C Demonstration Mean Time Between Failure : 30000 hours		

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	LIUTT		Wangdz

2018.4.30 GP-A50-F010