



Test Report: DDR-30L-12

30W DIN Rail Type DC-DC Converter

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Control Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

E.M.C. Test

■ RELIABILITY TEST

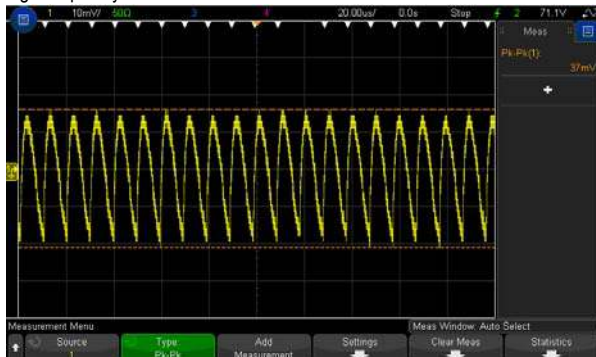
ENVIRONMENT TEST

DESIGN VERIFY TEST

OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OUTPUT VOLTAGE ADJUST RANGE	CH1:9.0 V~13.2 V	I/P : 48 VDC O/P : MIN LOAD Ta : 25°C	8.79V~13.52V
2	OUTPUT VOLTAGE TOLERANCE (Max)	V1: -2%~ 2%	I/P:18 VDC / 75VDC O/P:FULL/ MIN. LOAD Ta:25°C	V1: -0.31 %~ 0.32 %
3	LINE REGULATION (Max)	V1:-0.5%~ 0.5%	I/P: 18 VDC / 75VDC O/P:FULL LOAD Ta:25°C	V1: -0 %~ 0%
4	LOAD REGULATION (Max)	V1: -0.5%~ 0.5%	I/P: 48VDC O/P:FULL ~MIN LOAD Ta:25°C	V1: -0.31 %~ 0.32 %
5	OVER/UNDERSHOOT TEST	< $\pm 5\%$	I/P:48VDC O/P:FULL LOAD Ta:25°C	TEST: 1%
6	RIPPLE & NOISE (Max)	V1: 75 mVp-p	I/P: 48VDC O/P:FULL LOAD Ta:25°C	V1: 39 mVp-p

high frequency :



low frequency :

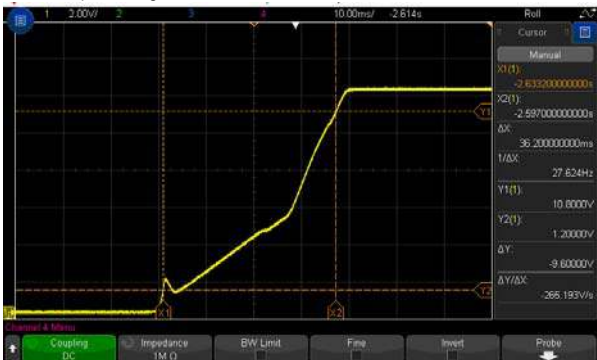
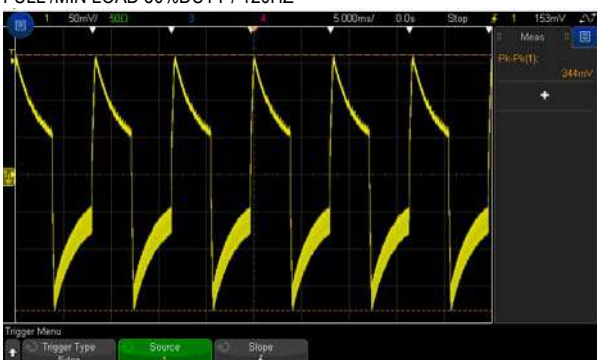
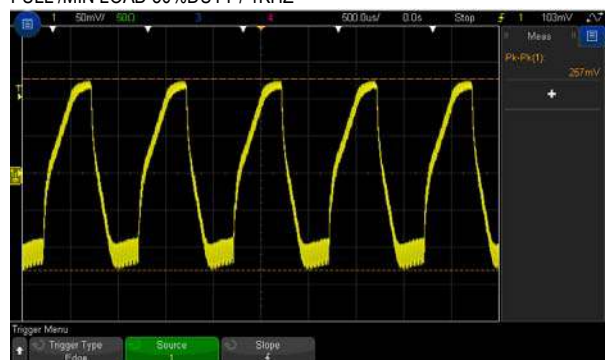


7	SET UP TIME (Max)	48VDC/120 ms	I/P:48 VDC O/P:FULL LOAD Ta:25°C	48VDC/ 58.8 ms
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INPUT=48VDC @ FULL LOAD

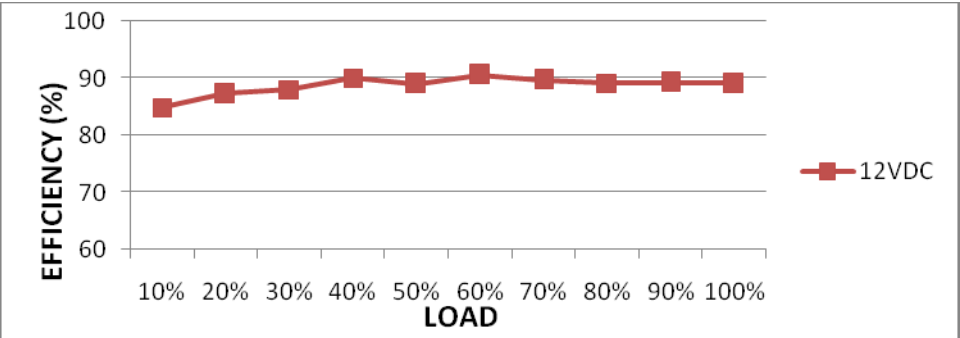
CH1 : Output Voltage CH4 :DC Input Voltage



8	RISE TIME (Max)	48VDC/ 85 ms	I/P: 48 VDC O/P:FULL LOAD Ta:25°C	48VDC/ 36.2 ms
	INPUT=48VDC@ FULL LOAD CH1 : Output Voltage 			
9	HOLD UP TIME (TYP)	48VDC/18ms	I/P: 48VDC O/P:FULL LOAD Ta:25°C	48VDC/ 22.2 ms
	INPUT=48VDC @ FULL LOAD CH1 : Output Voltage CH4 : DC Input Voltage 			
10	DYNAMIC LOAD	V1: 1200 mVp-p	I/P: 48VDC O/P: (1)FULL /MIN LOAD 50%DUTY / 120HZ (2)FULL /MIN LOAD 50%DUTY / 1KHZ Ta:25°C	344mVp-p 257mVp-p
	FULL /MIN LOAD 50%DUTY / 120HZ  FULL /MIN LOAD 50%DUTY / 1KHZ 			

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	18VDC~ 75 VDC	I/P:TESTING O/P:FULL LOAD Ta:25°C	17.665V~ 75V

			I/P: LOW-LINE-0.2=17.8V HIGH-LINE+3V=78V O/P:FULL/MIN LOAD (PLEASE CHECK DERATING CURVE) ON: 30 Sec . OFF: 30 Sec 10MIN (POWER ON/OFF NO DAMAGE)	TEST:OK
2	INPUT CURRENT(TYP)	48VDC/0.8 A	I/P: 48VDC O/P:FULL LOAD Ta:25°C	I =0.705A/48VDC
3	EFFICIENCY(TYP)	89%	I/P: 48VDC O/P:FULL LOAD Ta:25°C	89.09%
EFFICIENCY vs LOAD 				
4	INRUSH CURRENT(TYP)	48VDC/ 15 A COLD START	I/P: 48VDC O/P:FULL LOAD Ta:25°C	I =10.4A/ 48VDC
INPUT=48VDC @ FULL LOAD 				

PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER LOAD PROTECTION	110%~150%RATED OUTPUT POWER	I/P: 75VDC I/P: 48 VDC I/P: 18 VDC O/P:TESTING Ta:25°C	127.95%/ 75VDC 128%/ 48VDC 128.63%/ 18VDC PROTECTION TYPE : Constant current limiting, recovers automatically after fault condition is removed
2	OVER VOLTAGE PROTECTION	CH: 13.8V~ 16.2 V	I/P: 75VDC I/P: 48 VDC I/P: 18 VDC O/P:MIN LOAD Ta:25°C	15.18V/75VDC 15.18V/ 48VDC 15.18V/ 18VDC PROTECTION TYPE : Shut down O/P voltage,re-power on to recover

3	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 75 VDC O/P: FULL LOAD Ta:25°C	NO DAMAGE PROTECTION TYPE : Constant current limiting, recovers automatically after fault condition is removed
4	INPUT REVERSE	POWER OK	I/P:75VDC O/P: NO LOAD Ta:25°C	NO DAMAGE

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Transistor (D to S) or (C to E) Peak Voltage	Q 3 Rated: 150 V	I/P:High-Line +3V =78V DC ON/OFF VDS: O/P: (1)Full Load (2)Output Short (3) full load continue Ta : 25°C	VDS: (1)128.6V (2)105.3V (3)113.3V
2	Diode Peak Voltage	Q100 Rated: 100V	I/P:High-Line +3V =78 V DC ON/OFF O/P: (1)Full Load (2)Output Short (3)full load continue Ta : 25°C	VDS: (1)61.1V (2)47.8V (3)60.7V
3	Input Capacitor Voltage	C5 Rated: 560 μ / 80V	I/P:High-Line +3V =78 V O/P: (1)Full Load input on/off (2) Min load input on /Off (3)Full Load /Min load Change (4)Full load continue Ta : 25°C	C5: (1)79.8V (2)79.6V (3)79.8V (4)79.8V
4	Control IC Voltage Test	PWM IC U1 Rated: -0.3V~30V	I/P:High-Line +3V =78 V DC ON/OFF O/P(1)FULL LOAD (2) Output Short (3)O.L.P (4)O.V.P. Ta : 25°C	U1: (1) 15.4V (2) 9V (3) 15.2V (4) 18.2V
5	Clamp Diode Peak Voltage	D4 Rated : 400V	I/P : High-Line +3V = 78 V DC ON/OFF O/P : (1) Dynamic Load 90%Duty/1KHz (2)Full load continue Ta : 25°C	D4: (1)82.8V (2)82V

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	EN 60950-1 I/P-O/P:4KVDC/min	I/P-O/P: 4.4KVDC/min Ta:25°C	I/P-O/P: 0 mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P:500VDC>100M Ω	I/P-O/P: 500 VDC Ta:25°C	I/P-O/P: 9999M Ω NO DAMAGE

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RADIATION	☒ EN55032 ☐ EN55011 ☐ CLASS A ☒ CLASS B	I/P:48 VDC O/P:FULL LOAD Ta:25℃	☒ PASS ☐ FAIL Test by certified Lab
2	CONDUCTION	☒ EN55032 ☐ EN55011 ☐ CLASS A ☒ CLASS B	I/P: 48 VDC O/P:FULL LOAD Ta:25℃	☒ PASS ☐ FAIL Test by certified Lab
3	E.S.D	EN61000-4-2 ☐ Din rail Model : AIR: 8KV / Contact: 6KV	I/P:48VDC O/P:FULL LOAD Ta:25℃	☒ CRITERIA A ☐ CRITERIA B
4	E.F.T	EN61000-4-4 ☐ INDUSTRY INPUT: 2KV	I/P:48 VDC O/P:FULL LOAD Ta:25℃	☒ CRITERIA A ☐ CRITERIA B
5	SURGE	IEC61000-4-5 ☐ INDUSTRY line-line :1KV	I/P:48 VDC O/P:FULL LOAD Ta:25℃	☒ CRITERIA A ☐ CRITERIA B
6	Test by certified Lab & Test Report Prepare			

■ RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																				
1	TEMPERATURE RISE TEST	MODEL : DDR-30L-24 1. ROOM AMBIENT BURN-IN : 1 HRS I/P : 48VDC O/P : FULL LOAD Ta= 22.2 ℃ 2. HIGH AMBIENT BURN-IN : 1 HRS I/P : 48VDC O/P : FULL LOAD Ta= 59.4 ℃ <table><tr><th>NO</th><th>Position</th><th>ROOM AMBIENT Ta= 22.2 ℃</th><th>HIGH AMBIENT Ta= 59.4 ℃</th></tr><tr><td>1</td><td>LF1</td><td>37.8℃</td><td>78.4℃</td></tr><tr><td>2</td><td>T1</td><td>59.4℃</td><td>96.1℃</td></tr><tr><td>3</td><td>T2</td><td>51.8℃</td><td>91.2℃</td></tr><tr><td>4</td><td>L100</td><td>40.8℃</td><td>76.5℃</td></tr><tr><td>5</td><td>Q2</td><td>33.9℃</td><td>72.2℃</td></tr><tr><td>6</td><td>Q3</td><td>62.7℃</td><td>109.1℃</td></tr><tr><td>7</td><td>Q100</td><td>68.9℃</td><td>107.2℃</td></tr><tr><td>8</td><td>D4</td><td>66.4℃</td><td>105.5℃</td></tr><tr><td>9</td><td>R7</td><td>67.2℃</td><td>105.9℃</td></tr><tr><td>10</td><td>C15</td><td>61.0℃</td><td>99.1℃</td></tr><tr><td>11</td><td>U1</td><td>52.7℃</td><td>95.7℃</td></tr><tr><td>12</td><td>C5</td><td>41.6℃</td><td>79.4℃</td></tr><tr><td>13</td><td>C105</td><td>55.8℃</td><td>90.0℃</td></tr><tr><td>14</td><td>C106</td><td>51.5℃</td><td>86.4℃</td></tr><tr><td>15</td><td>C40</td><td>51.9℃</td><td>87.3℃</td></tr><tr><td>16</td><td>C110</td><td>53.6℃</td><td>88.5℃</td></tr></table>			NO	Position	ROOM AMBIENT Ta= 22.2 ℃	HIGH AMBIENT Ta= 59.4 ℃	1	LF1	37.8℃	78.4℃	2	T1	59.4℃	96.1℃	3	T2	51.8℃	91.2℃	4	L100	40.8℃	76.5℃	5	Q2	33.9℃	72.2℃	6	Q3	62.7℃	109.1℃	7	Q100	68.9℃	107.2℃	8	D4	66.4℃	105.5℃	9	R7	67.2℃	105.9℃	10	C15	61.0℃	99.1℃	11	U1	52.7℃	95.7℃	12	C5	41.6℃	79.4℃	13	C105	55.8℃	90.0℃	14	C106	51.5℃	86.4℃	15	C40	51.9℃	87.3℃	16	C110	53.6℃	88.5℃
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2	OVER LOAD BURN-IN TEST	NO DAMAGE 1 HOUR (MIN)	I/P : 48 VDC O/P : 123 % LOAD Ta : 25℃	TEST : OK																																																																				

3	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 24 VDC/ 75 VDC O/P : 100 % LOAD Ta= -45 °C	TEST : OK												
4	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60 °C NO DAMAGE	I/P : 78 VDC O/P : FULL LOAD Ta= 60 °C HUMIDITY= 95 %R.H	TEST : OK												
5	TEMPERATURE COEFFICIENT	± 0.03 %(0~60°C)	I/P : 48 VDC O/P : FULL LOAD	± 0.0022 %(0~60°C)												
6	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature : -45°C~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : STATIC		TEST : OK												
7	THERMAL SHOCK TEST	1. Thermal shock Temperature : -45°C~ +65°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 16 CYCLE 5. Input/Output condition : 48VDC/Full Load DC ON/OFF TEST turn on 3sec ; turn off 1sec@15cycle\ 48VDC/Full Load DC ON@1cycle		TEST : OK												
8	VIBRATION TEST	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 10min/sweep cycle (4) Acceleration : 3G (5) Test Time : 60min in each axis (X.Y.Z) (6) Ta : 25°C 2 Din Rail <table><tr><td></td><td>Displacement</td><td>Acceleration</td></tr><tr><td>2 (+3/-0) Hz up to 15Hz</td><td>± 2.5mm</td><td>-----</td></tr><tr><td>15Hz up to 50Hz</td><td>-----</td><td>2.3g</td></tr><tr><td>Sweep rate</td><td colspan="2">Max 1 Octave/minute</td></tr></table>			Displacement	Acceleration	2 (+3/-0) Hz up to 15Hz	± 2.5mm	-----	15Hz up to 50Hz	-----	2.3g	Sweep rate	Max 1 Octave/minute		TEST : OK
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Sweep rate	Max 1 Octave/minute															
9	CAPACITOR LIFE CYCLE	SUPPOSE C105 IS THE MOST CRITICAL COMPONENT (1) I/P : 48VDC O/P : FULL LOAD Ta= 25 °C LIFE TIME (2) I/P : 48VDC O/P : FULL LOAD Ta= 60 °C LIFE TIME (3) I/P : 48VDC O/P : 75% LOAD Ta= 60 °C LIFE TIME (4) I/P : 48VDC O/P : 50% LOAD Ta= 60 °C LIFE TIME		(1) 219882.6 HRS (2) 23902.2 HRS (3) 37486.3 HRS (4) 56997.7 HRS												
10	MTBF	Conducted by Parts Stress Analysis Prediction 483.3K hrs min. MIL-HDBK-217F (25°C)														
11	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure (Expected Life): Above 30,000 hours @ TA 60°C														

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	LIUTT		WANGDZ

12.10.30 A50-F031